

FIG. 2

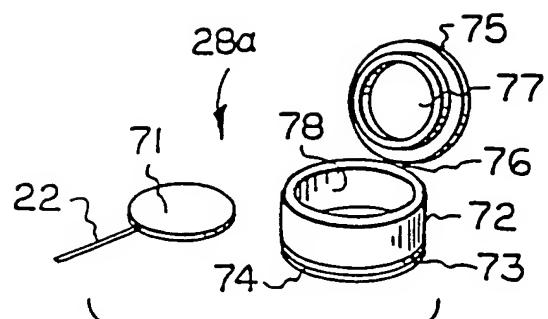


FIG. 11

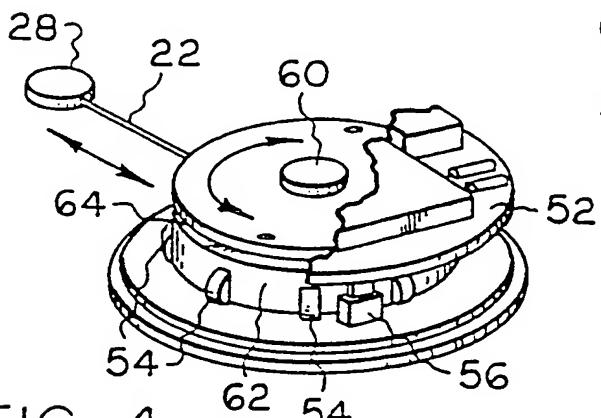


FIG. 4

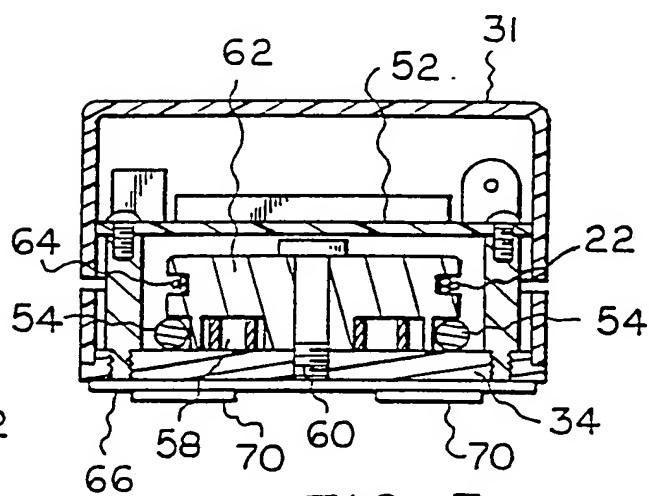


FIG. 3

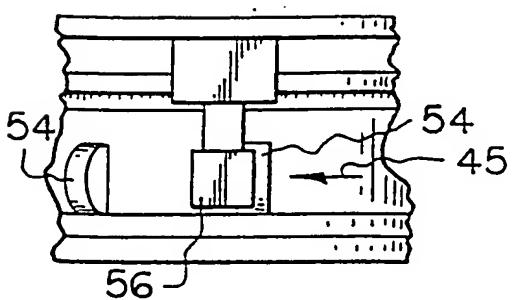


FIG. 5

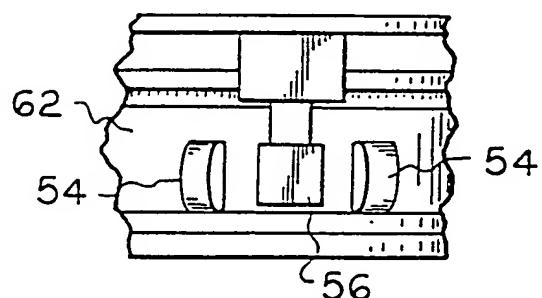
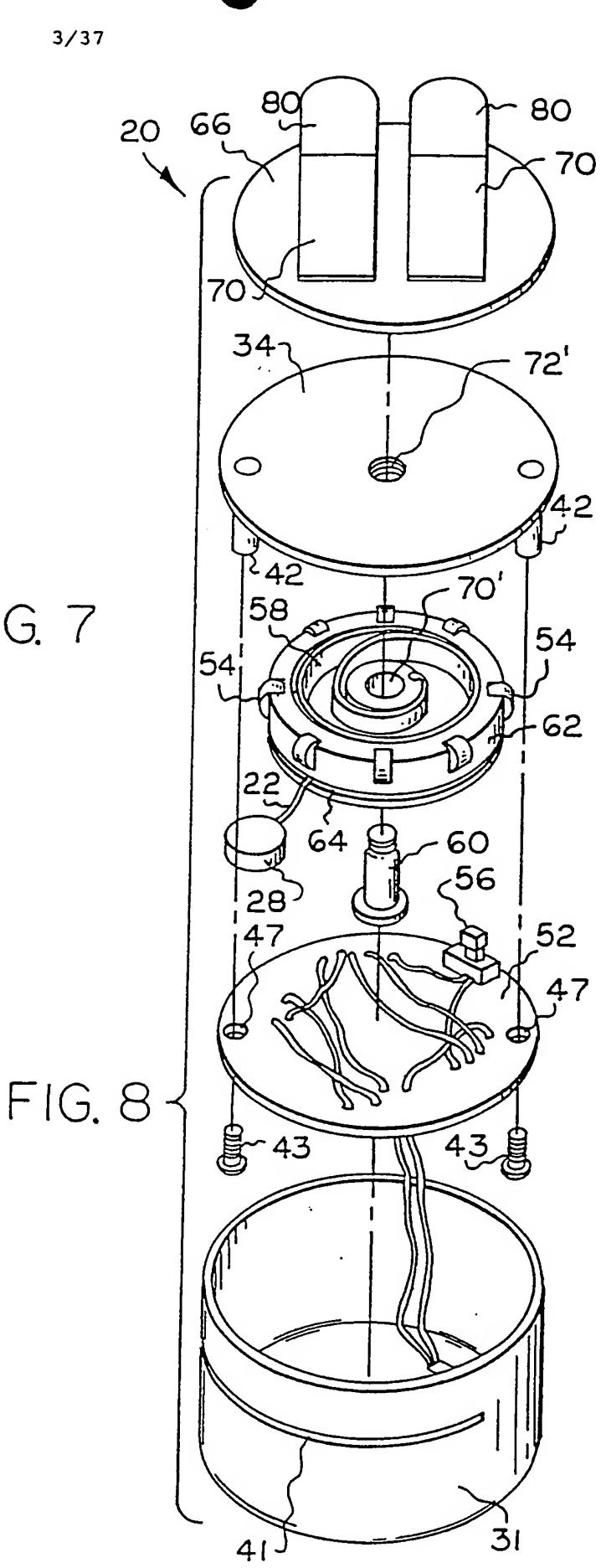
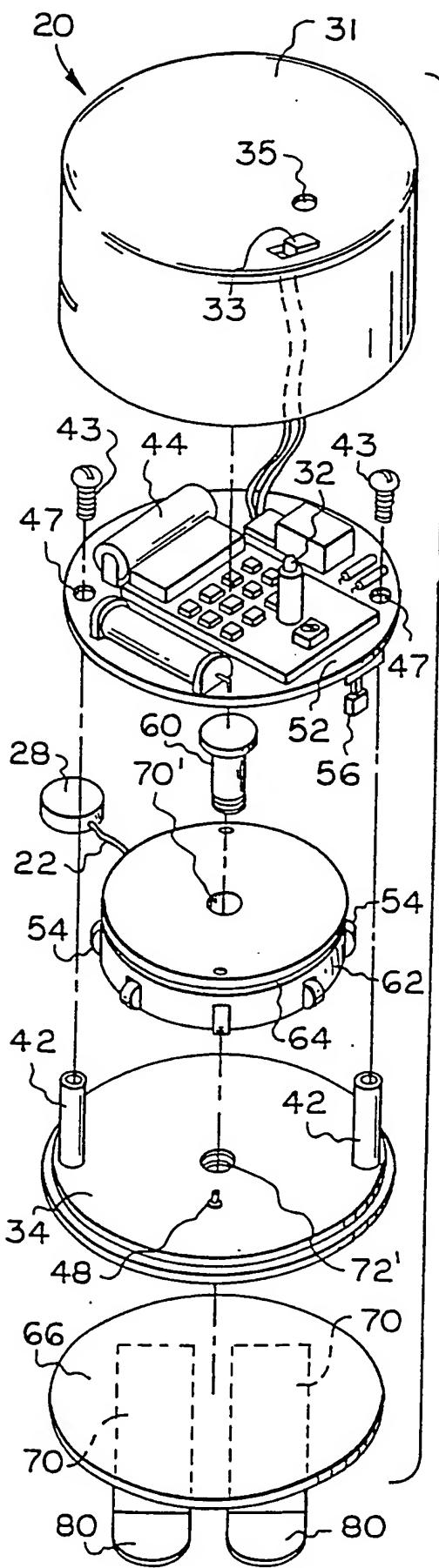


FIG. 6



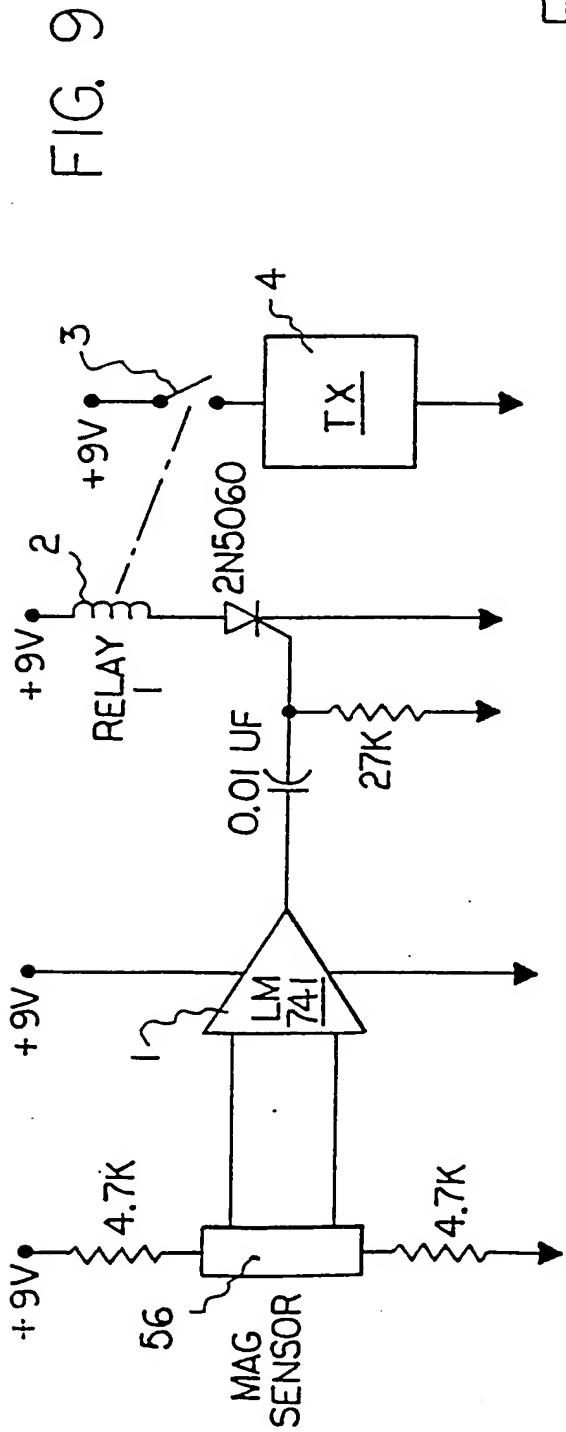
**FIG. 10**

FIG. 10

This circuit diagram illustrates a receiver system. An antenna is connected to the input of a receiver stage. The receiver's output is connected to the VCC pin of a Z3300 integrated circuit (IC). The Z3300 IC has several pins: J (V_{DD}), K (Z₂), CK (clock), Q (output), R (reset), S (set), and V_{SS}. The Q output is connected to the base of a transistor. The collector of this transistor is connected to the TX stage via a 27K resistor. The emitter of the transistor is connected to ground. A 2VDC power source is connected to the V_{DD} and V_{SS} pins of the Z3300. The Z₁ pin is connected to ground. The TONE ADJ pin is connected to the RST pin via a 4.7K resistor. The OUT pin is connected to the DISCH pin via a 4.7K resistor. The GND pin is connected to ground. The TX stage consists of a 2N5060 transistor driving a speaker (SPKR) through a 100UF capacitor and an 8Ω load. The TX stage also receives control signals from the Z3300's CK, Q, and R pins. A 0.047 UF capacitor is connected between the TX stage and ground.

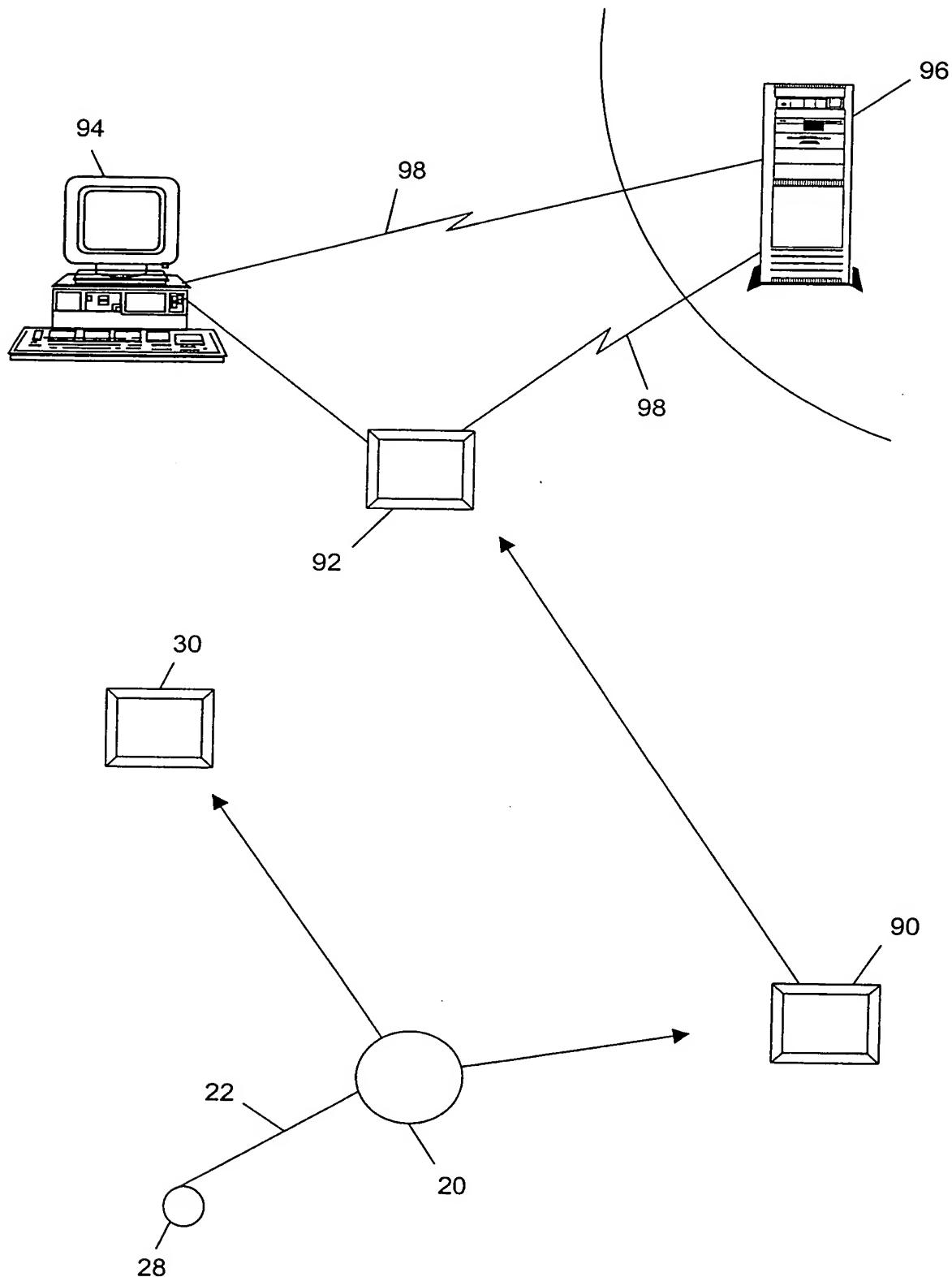


FIG. 12

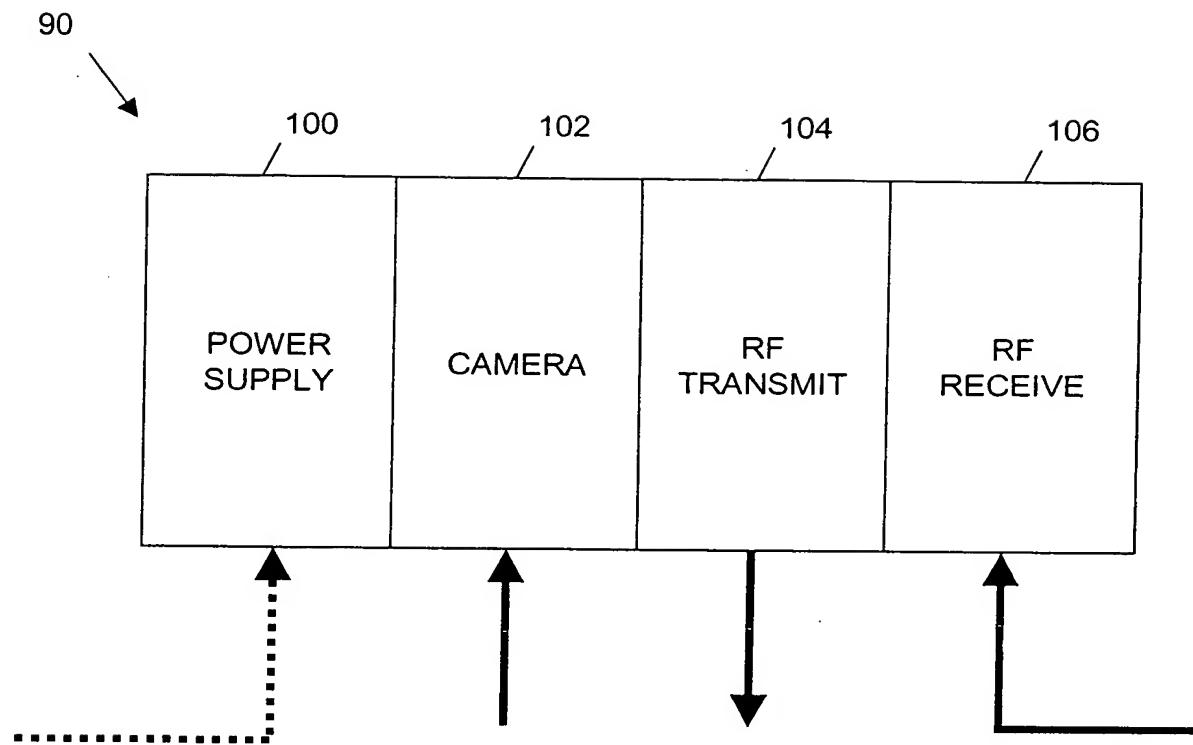


FIG. 13

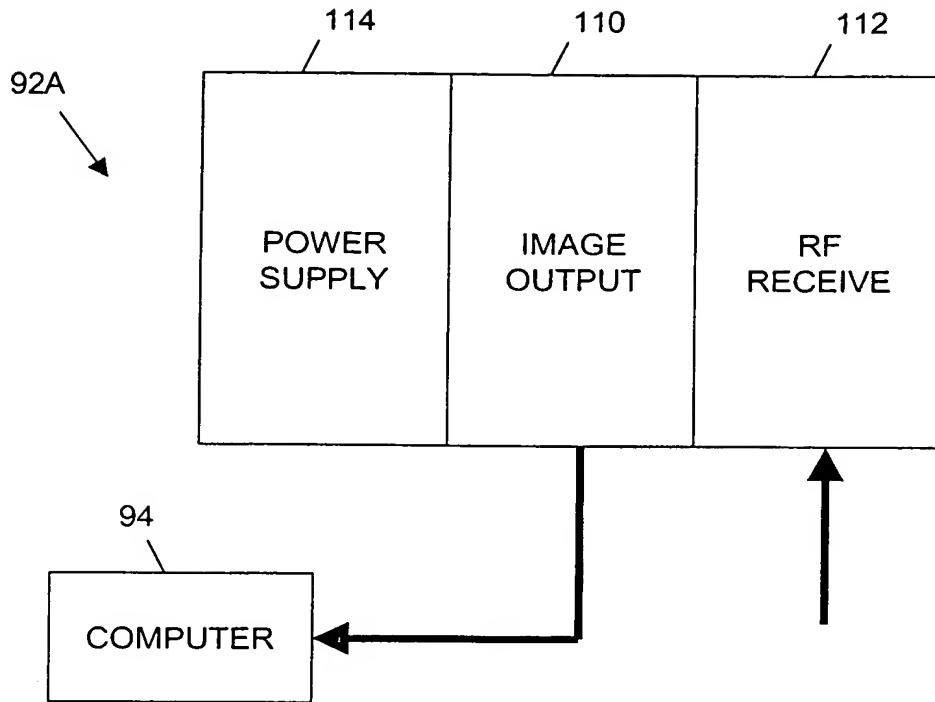


FIG. 14A

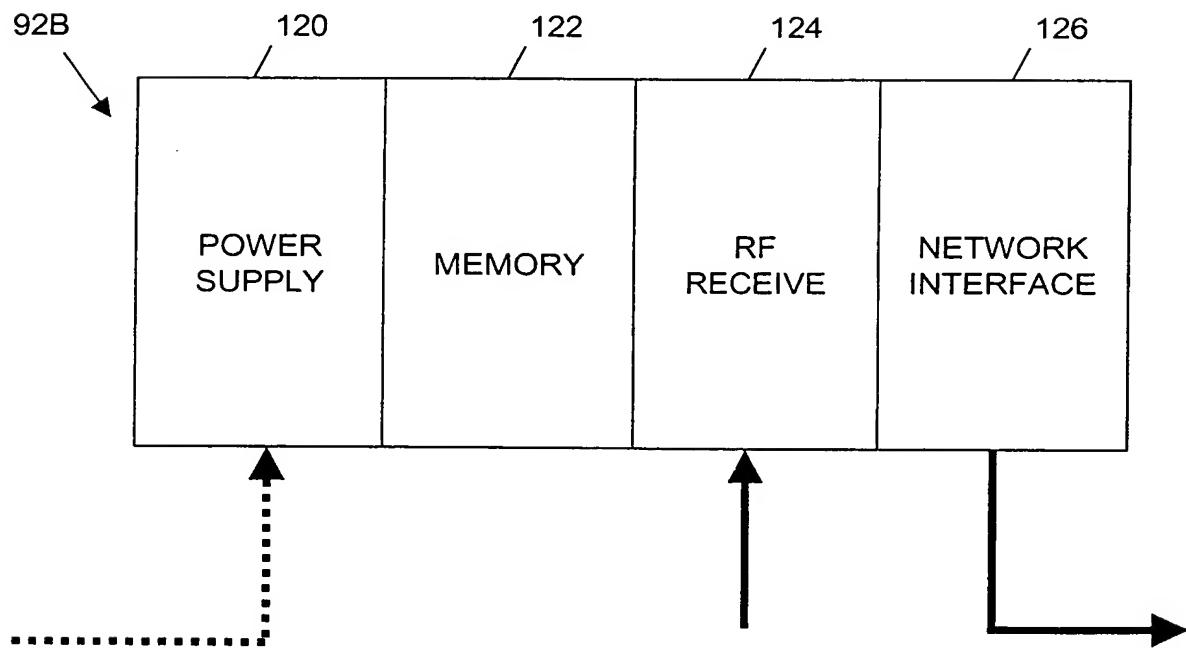


FIG. 14B

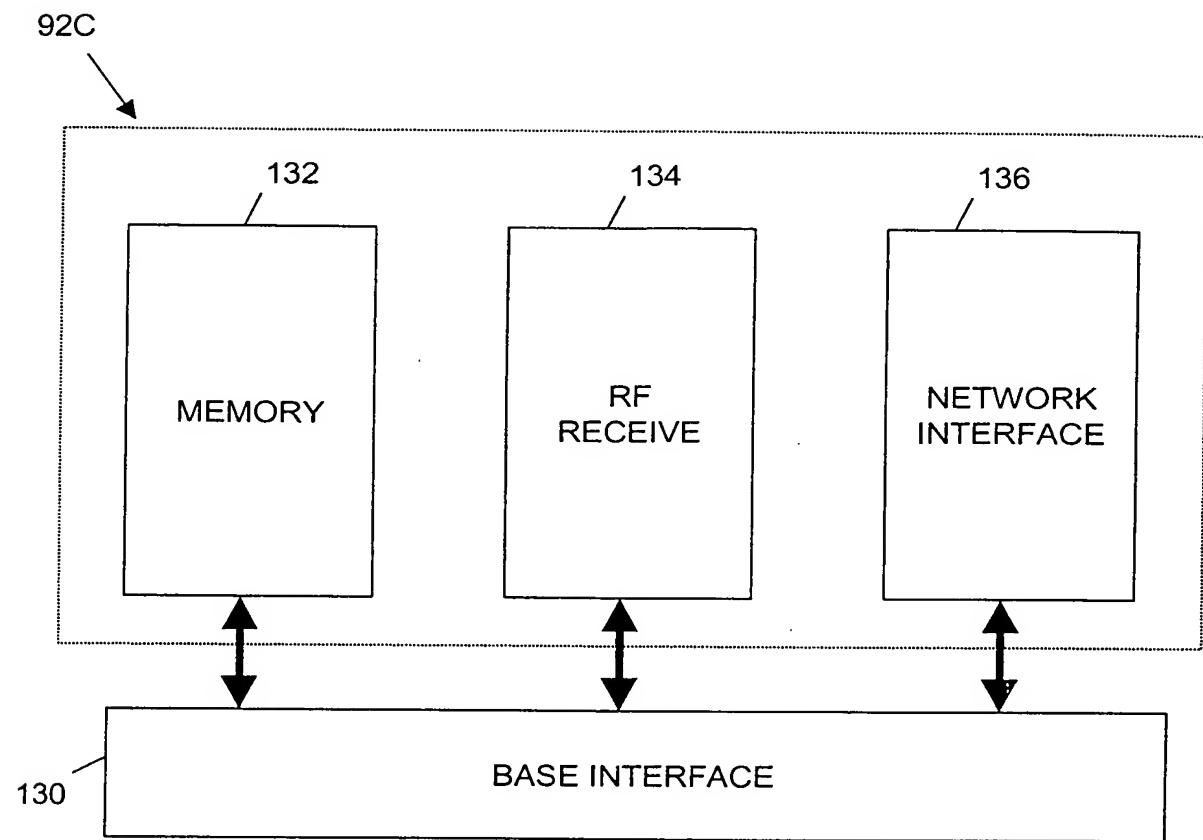


FIG. 14C

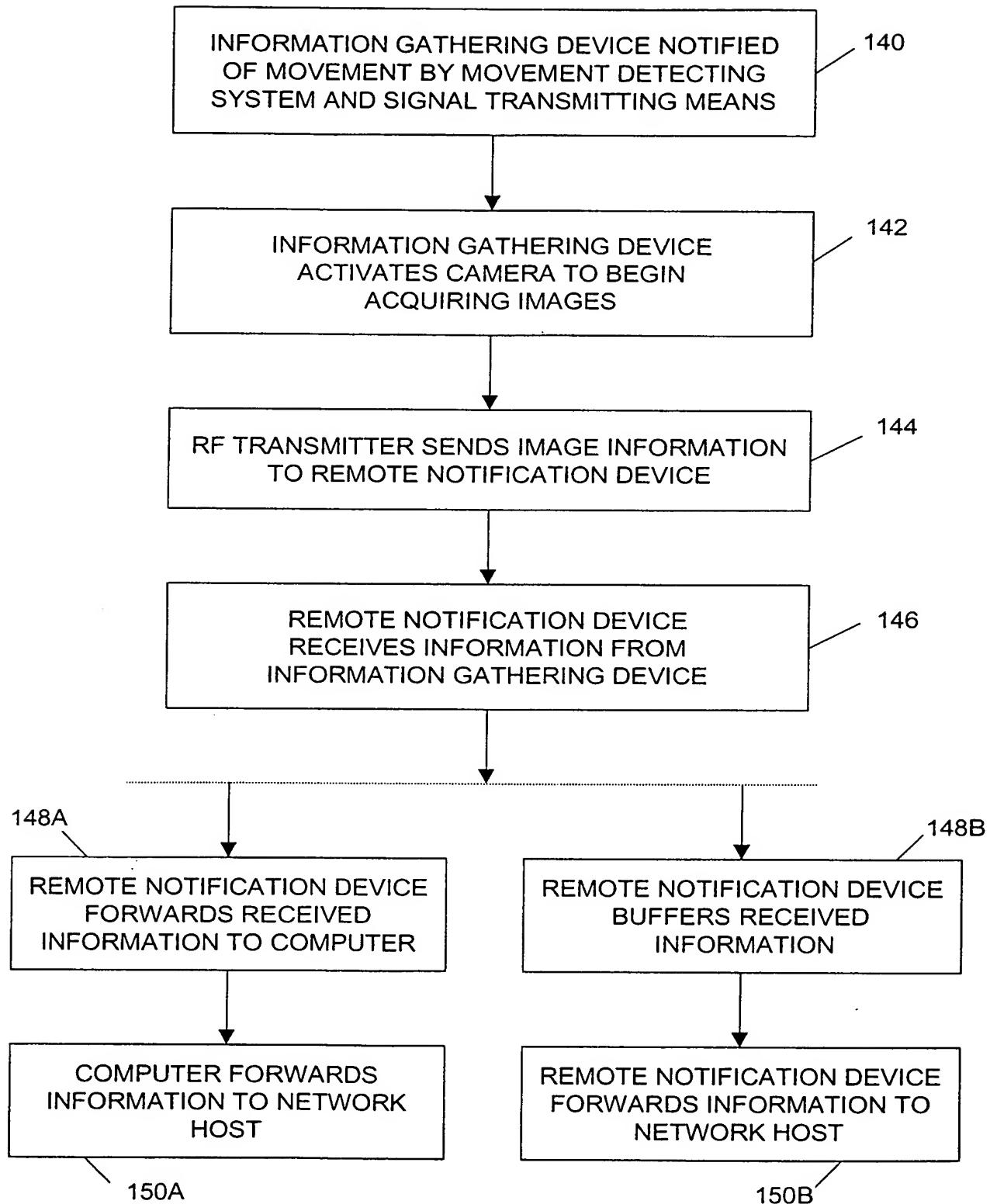


FIG. 15

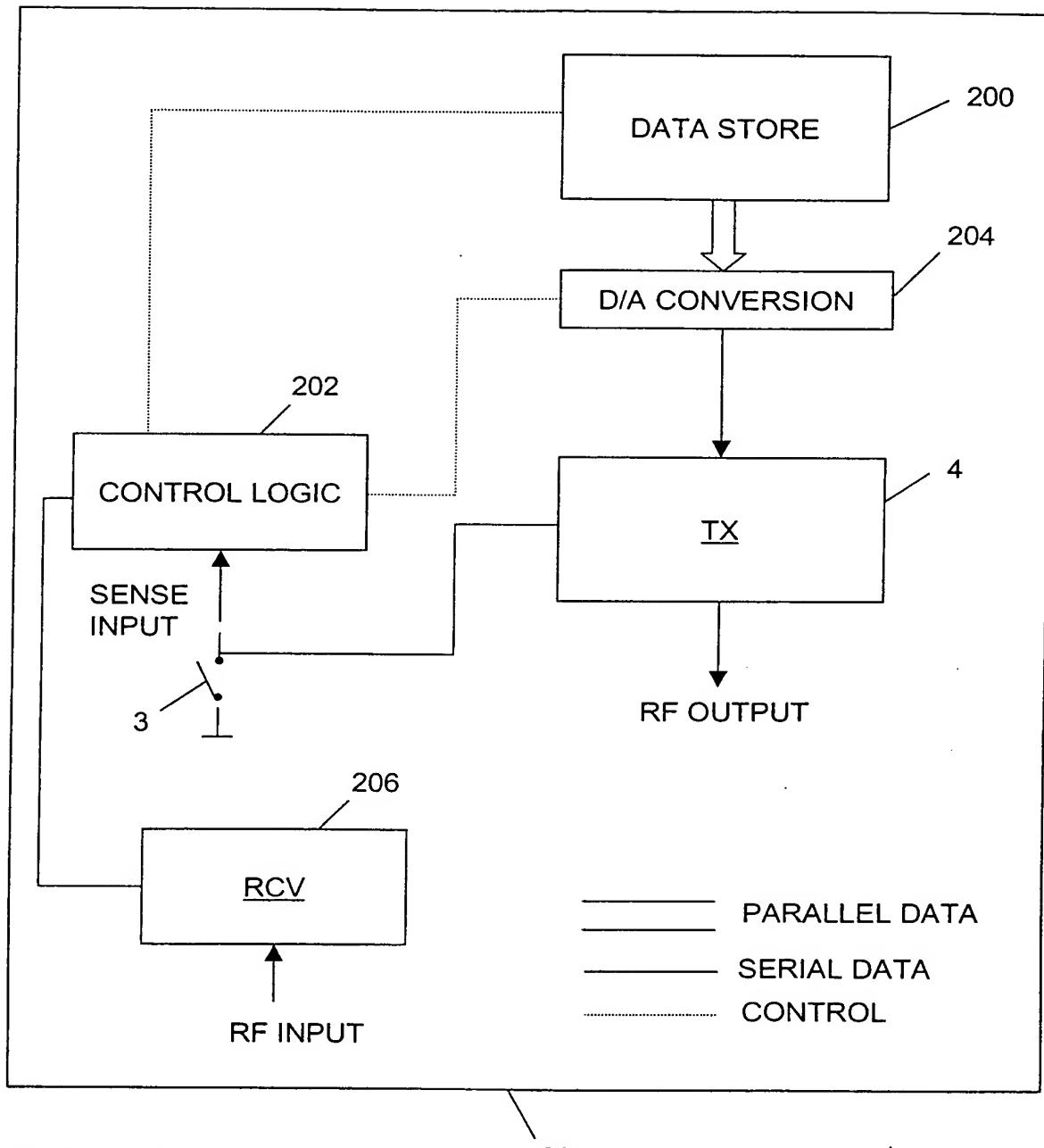


FIG. 16

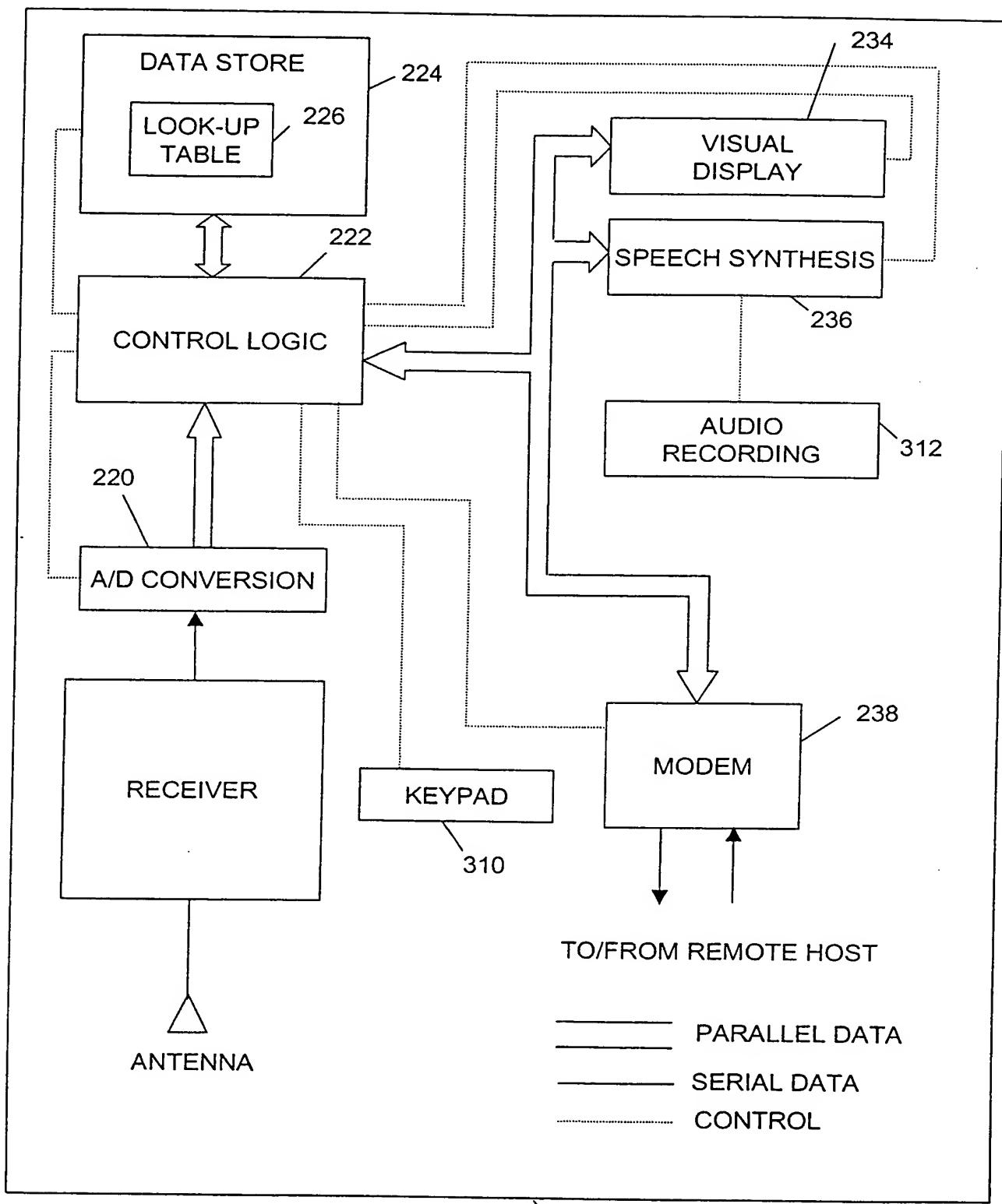


FIG. 17

xxxyyzz00	FRONT DOOR
xxxyyzz01	BACK DOOR
xxxyyzznn	ANYWHERE

FIG. 18

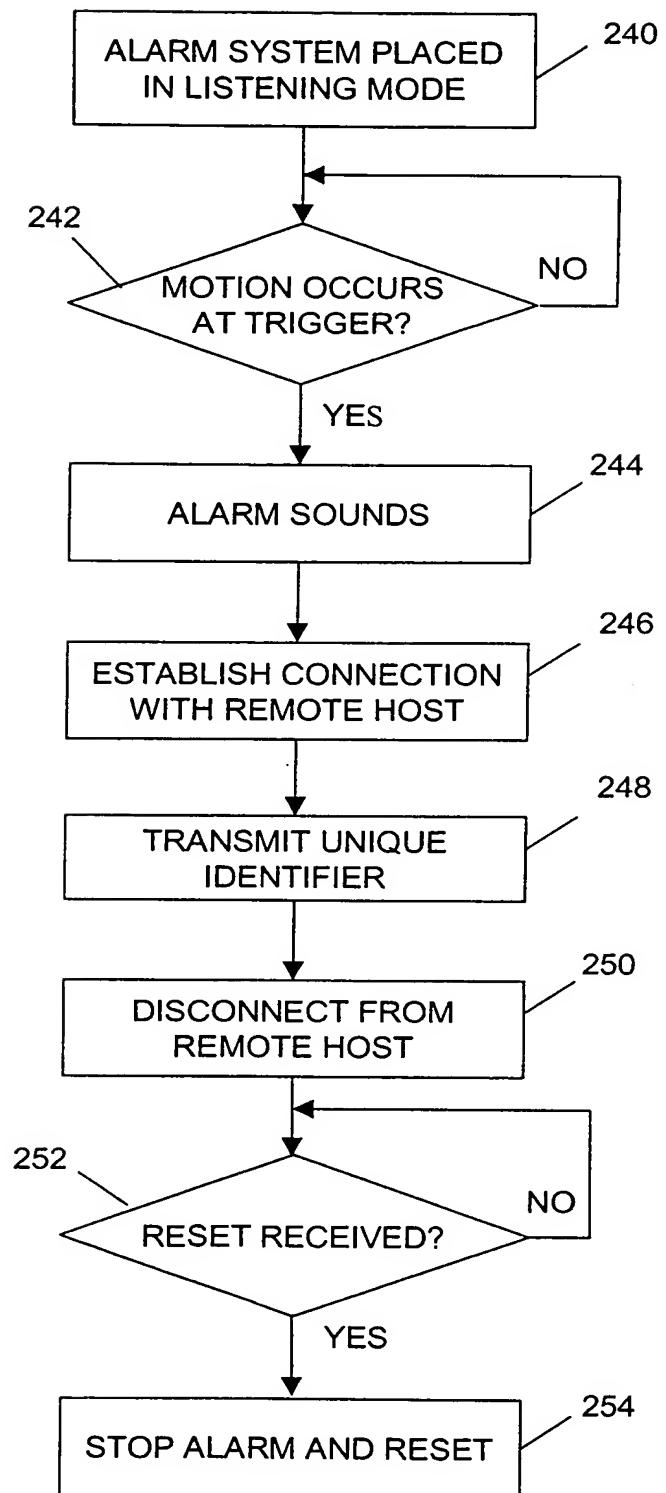


FIG. 19

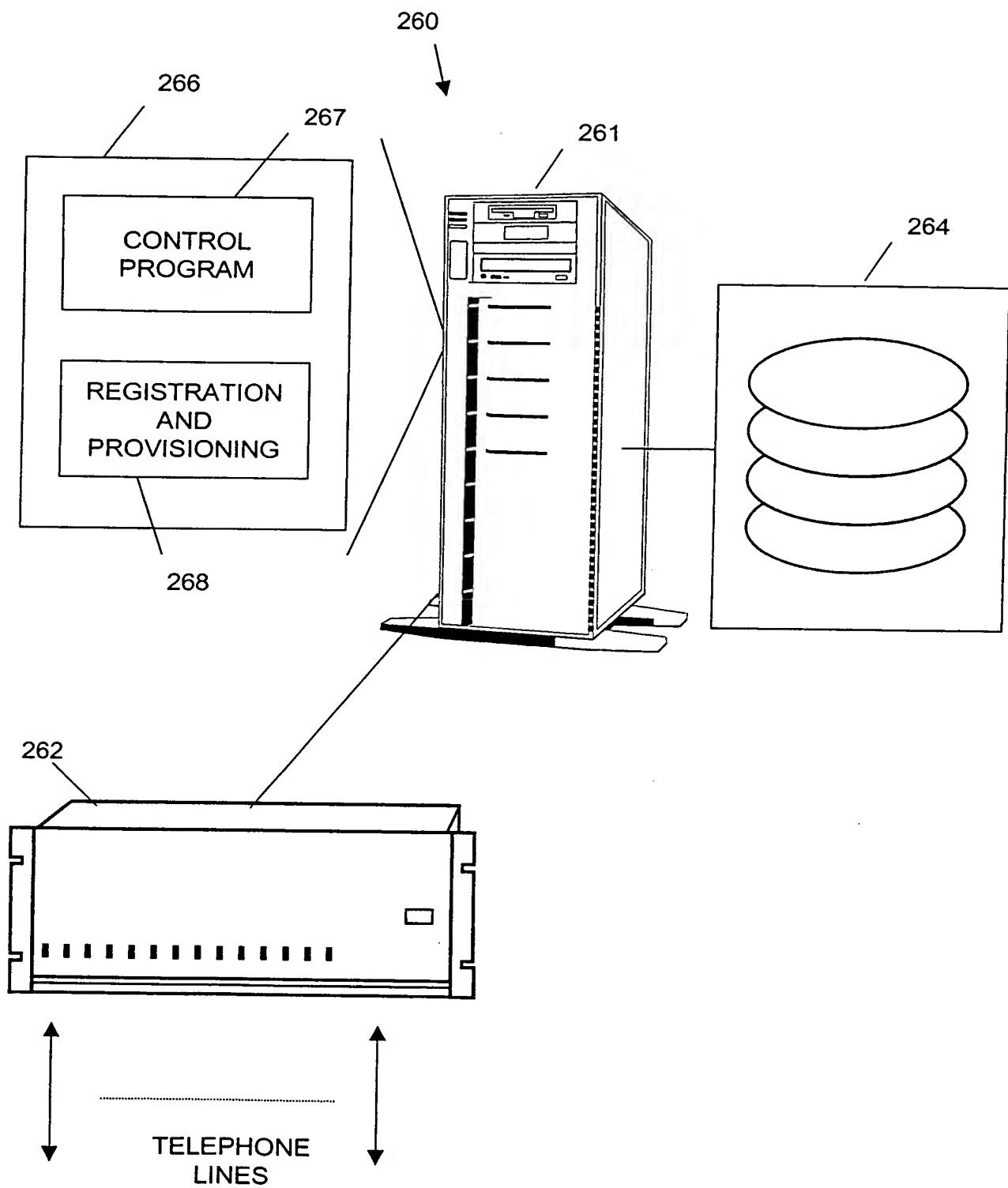


FIG. 20

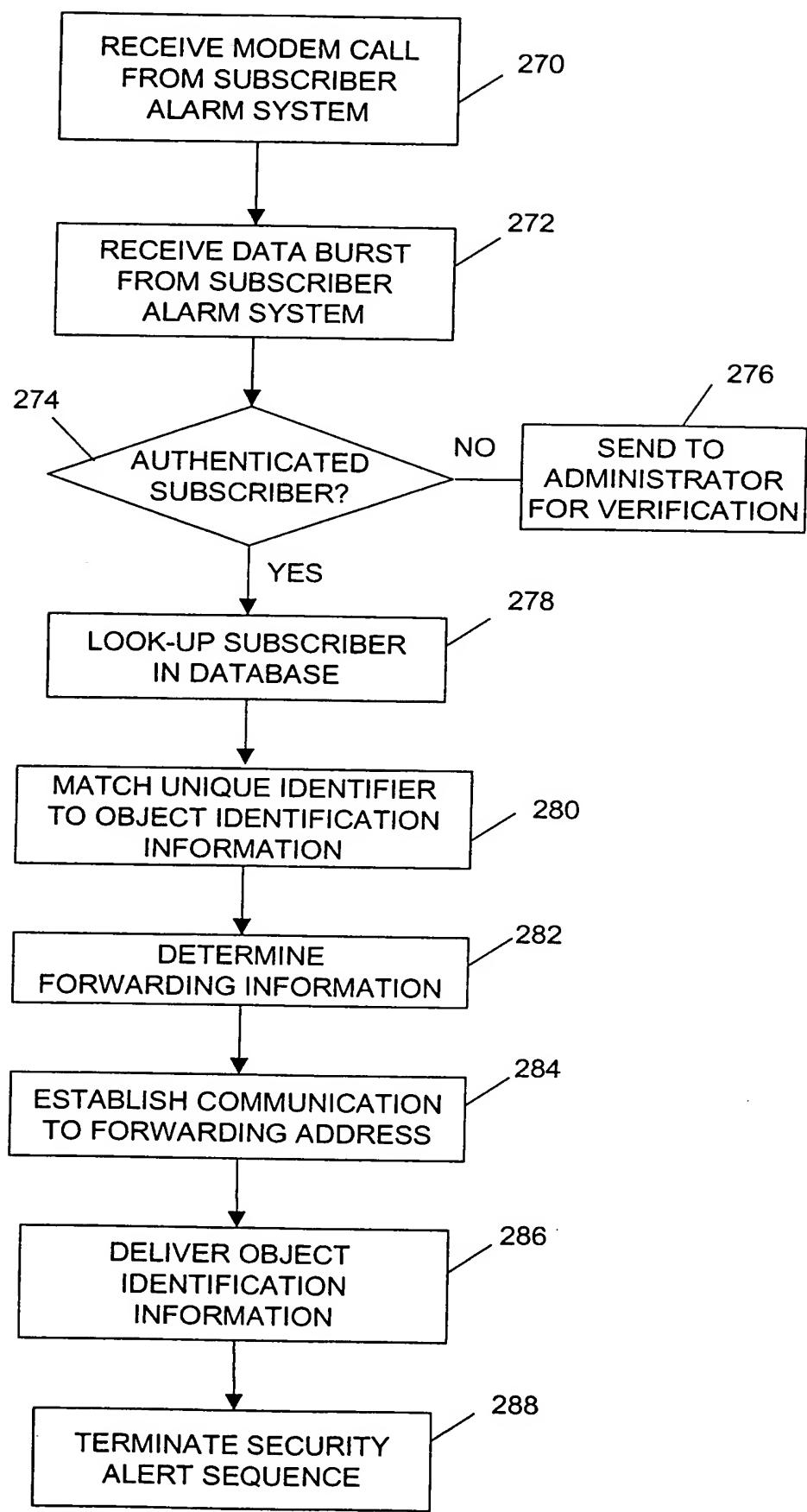


FIG. 21

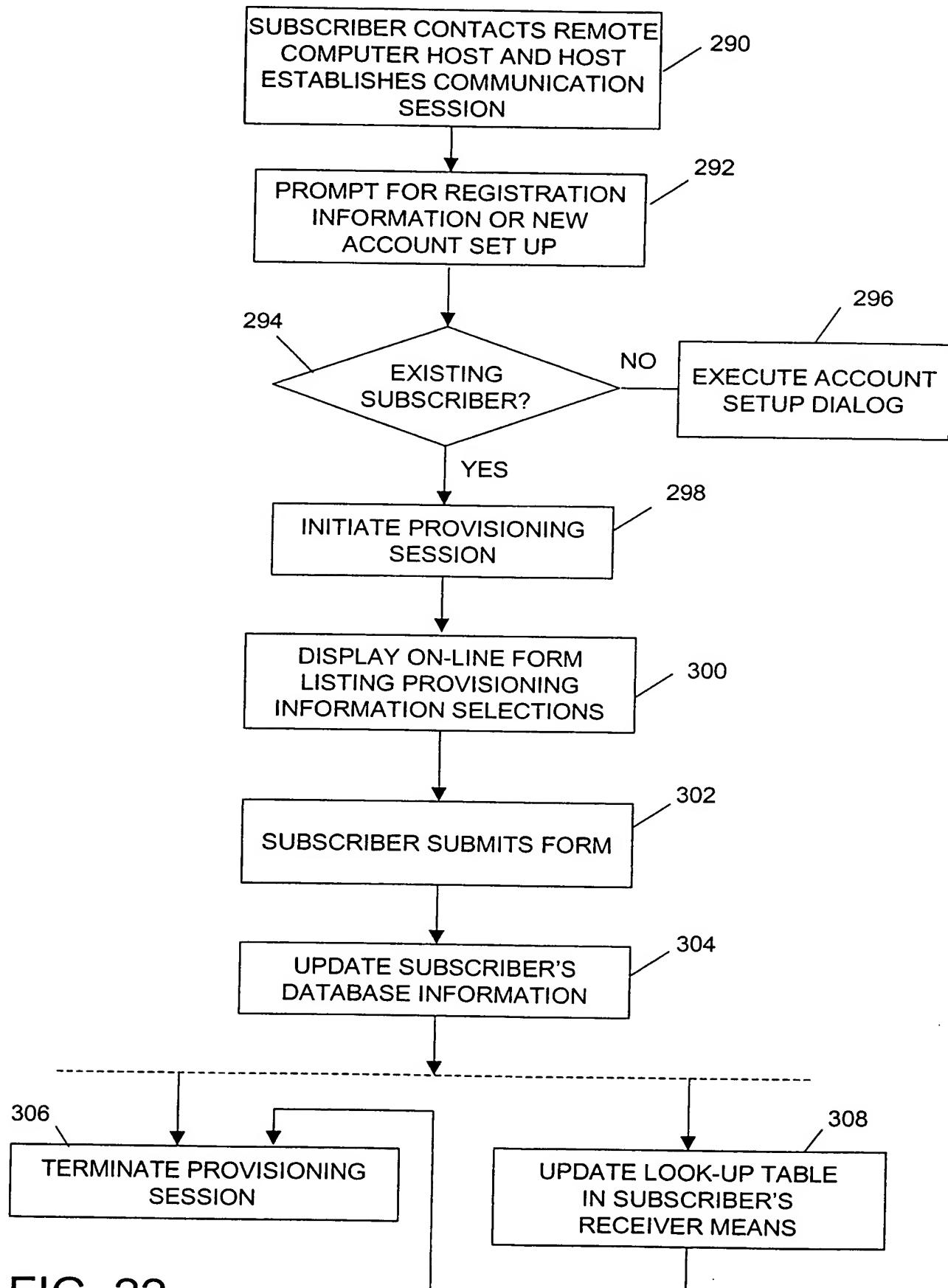


FIG. 22

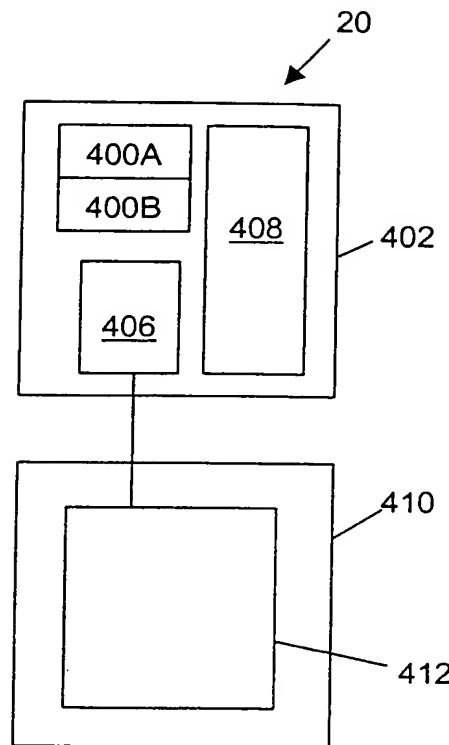


FIG. 23

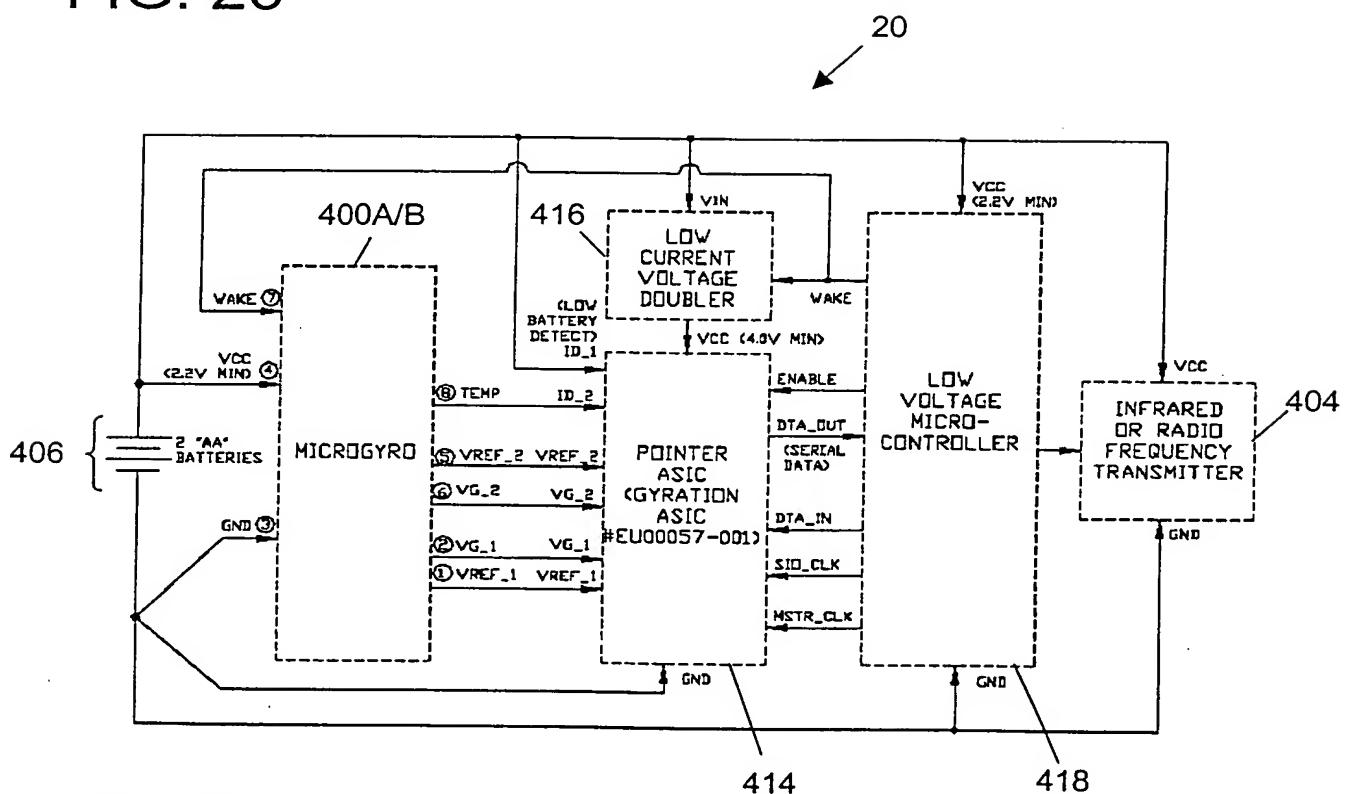


FIG. 24

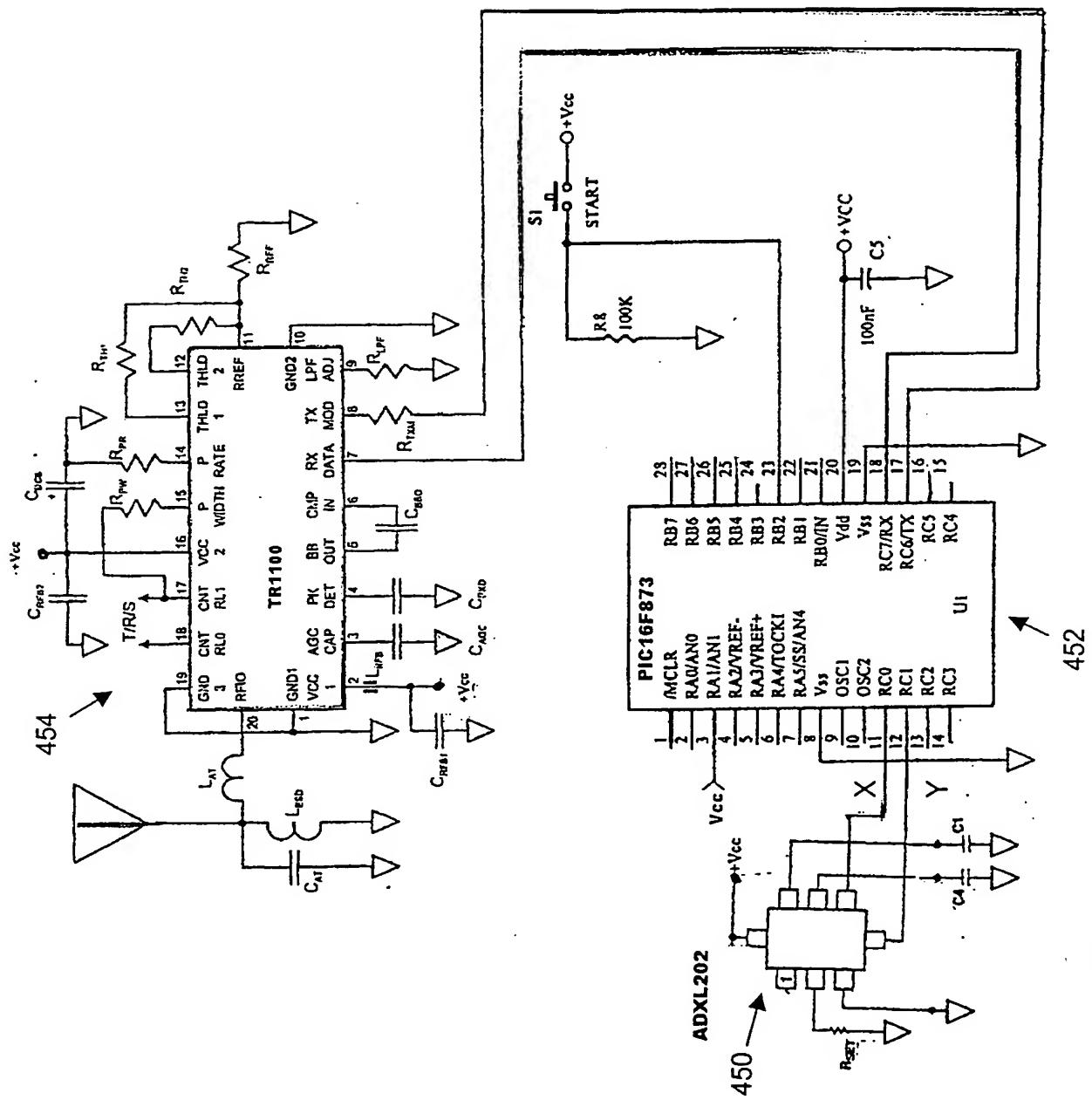


FIG. 25

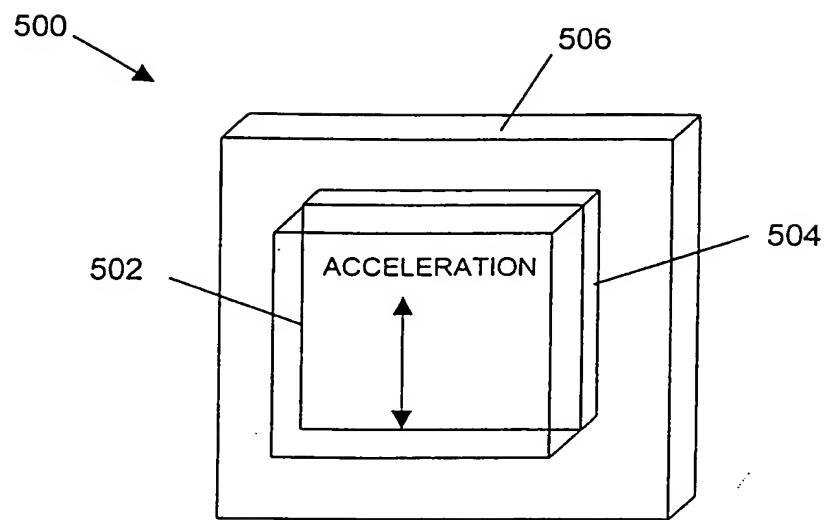


FIG. 26

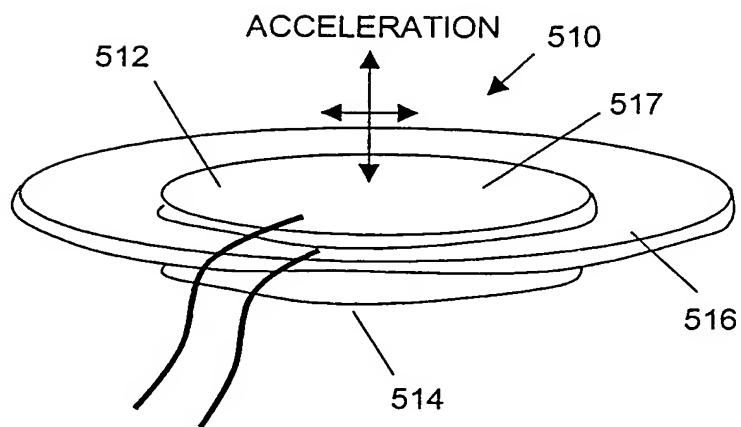


FIG. 27

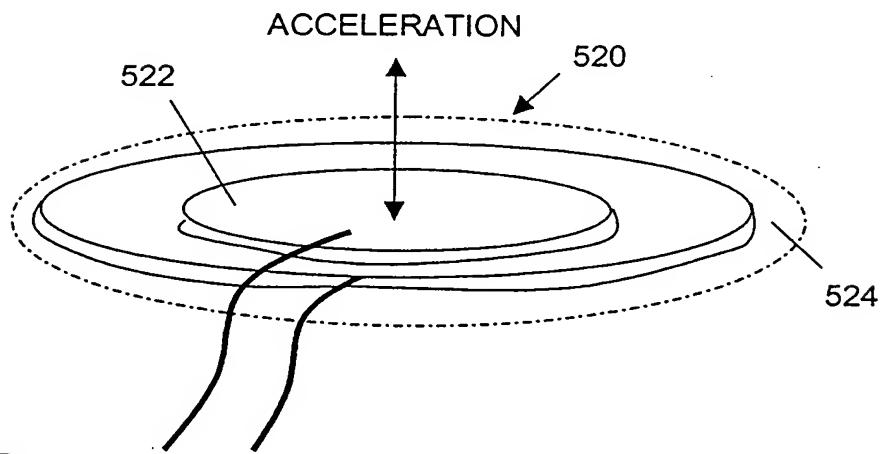


FIG. 28

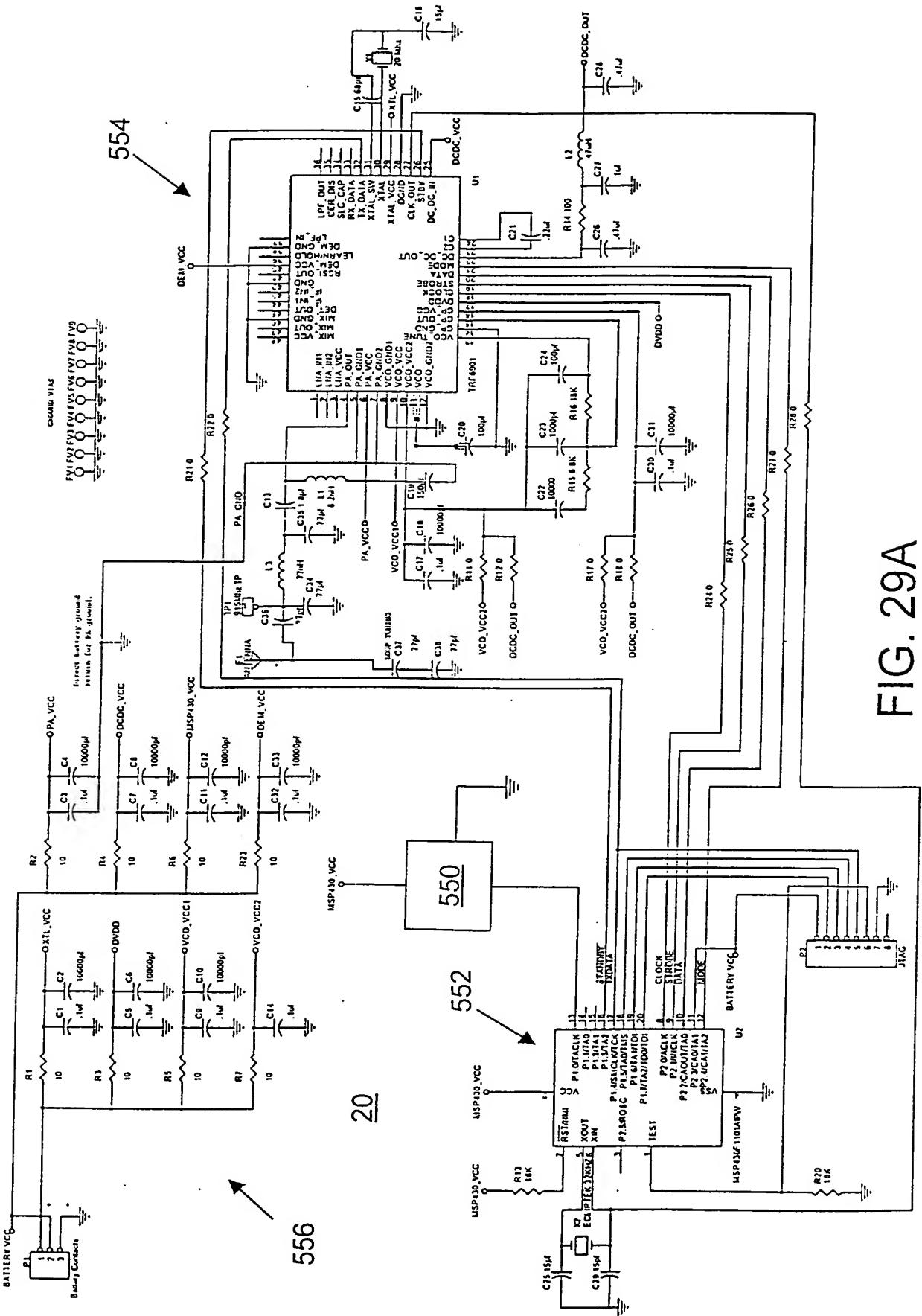


FIG. 29A

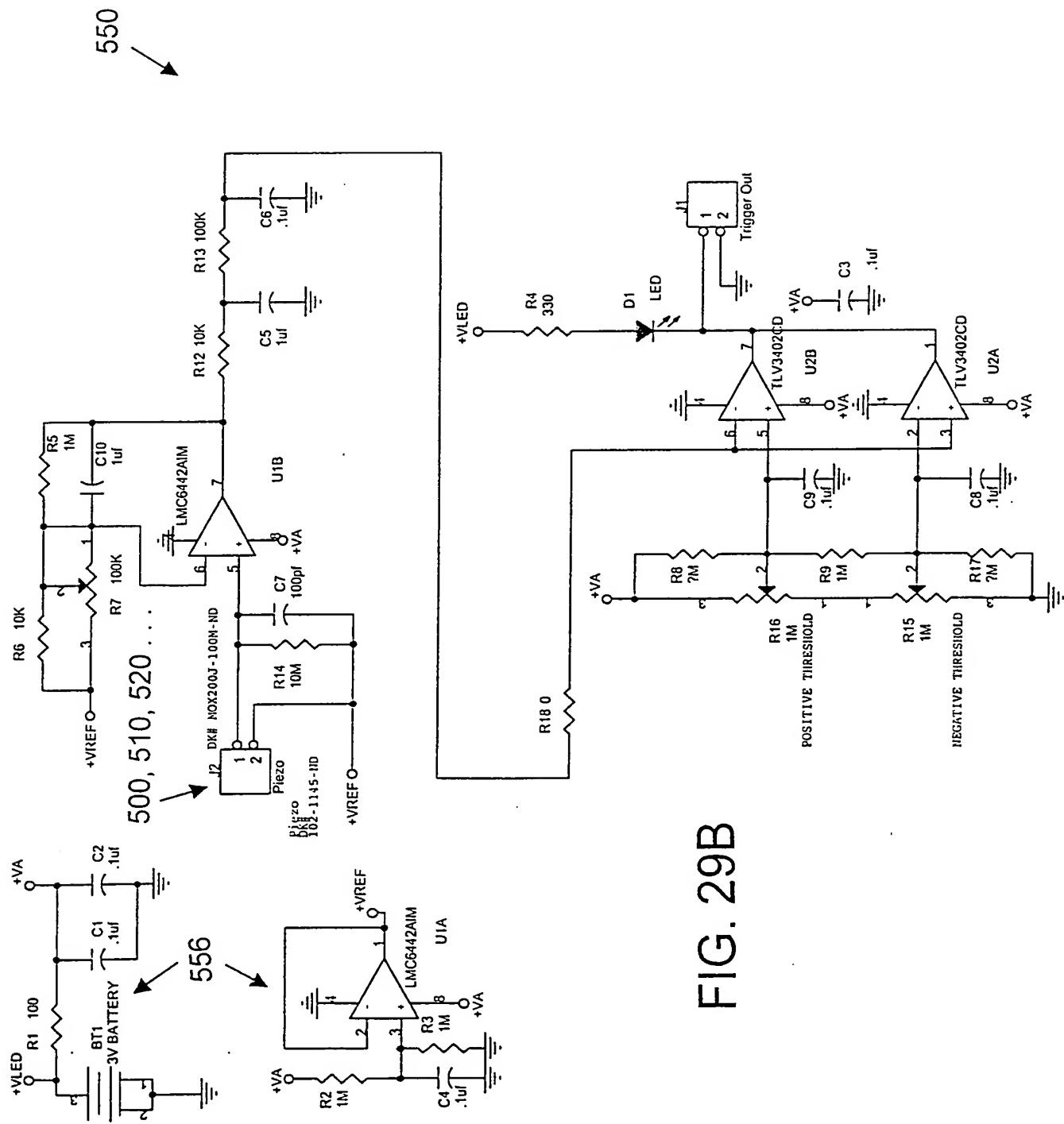


FIG. 29B

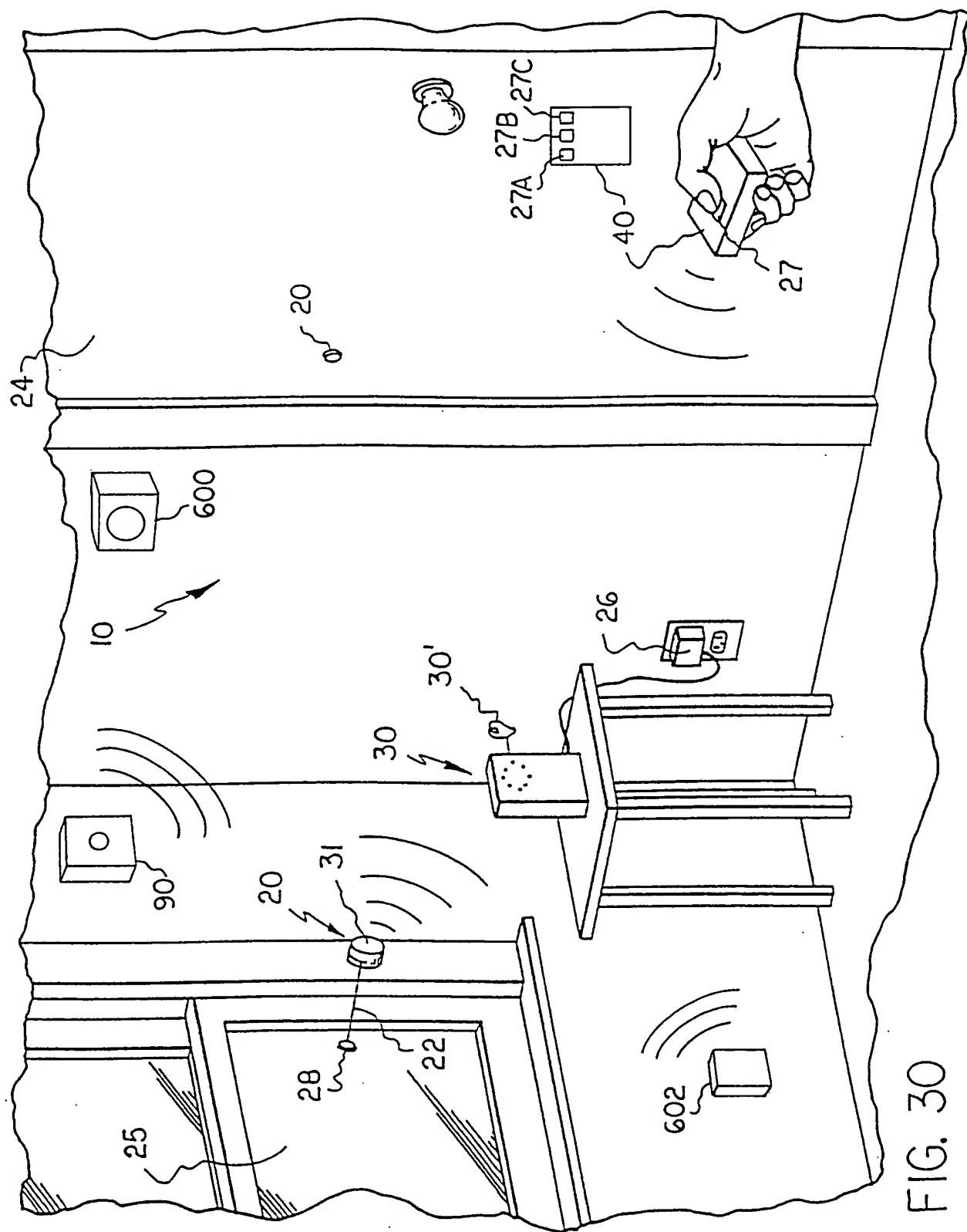


FIG. 30

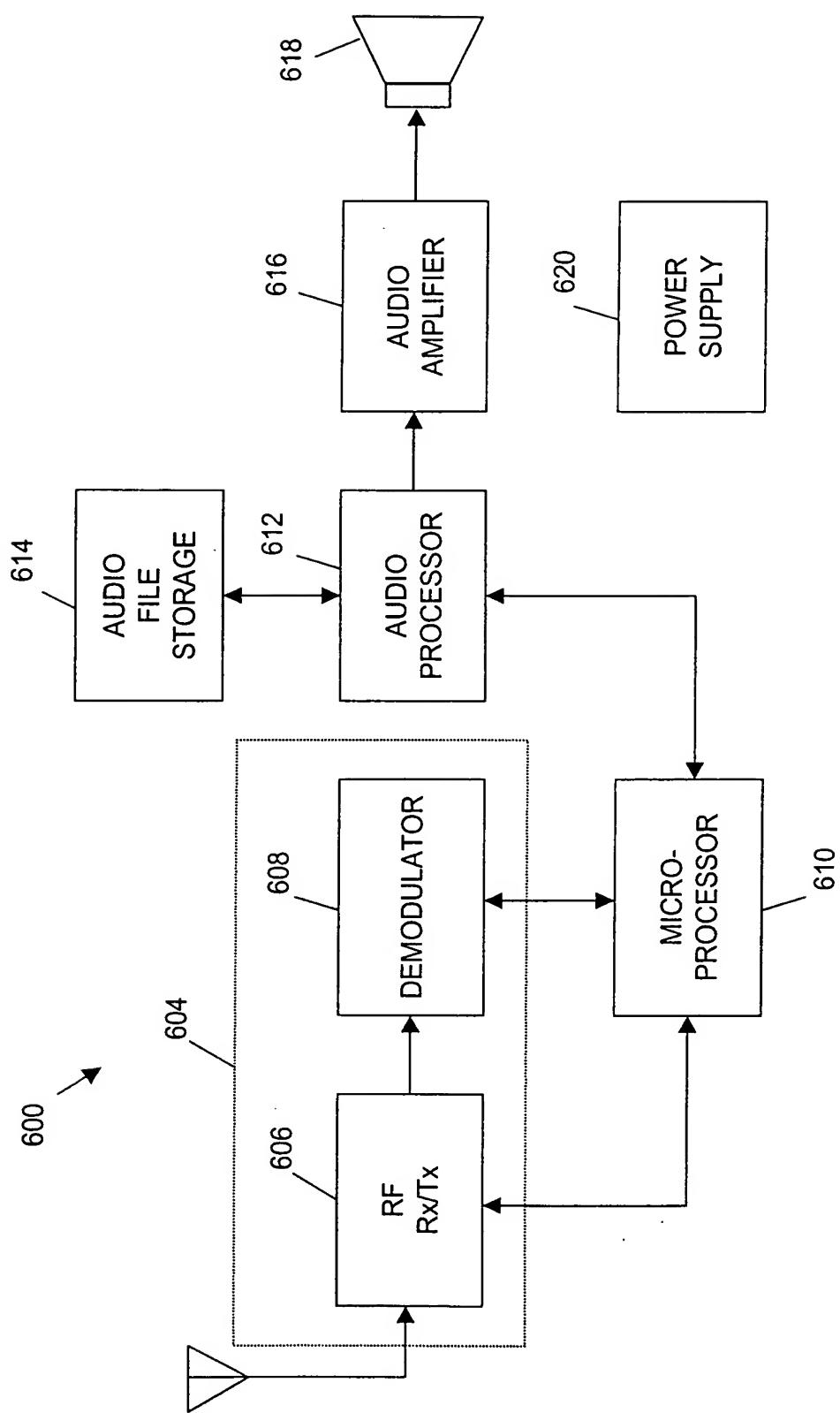


FIG. 31

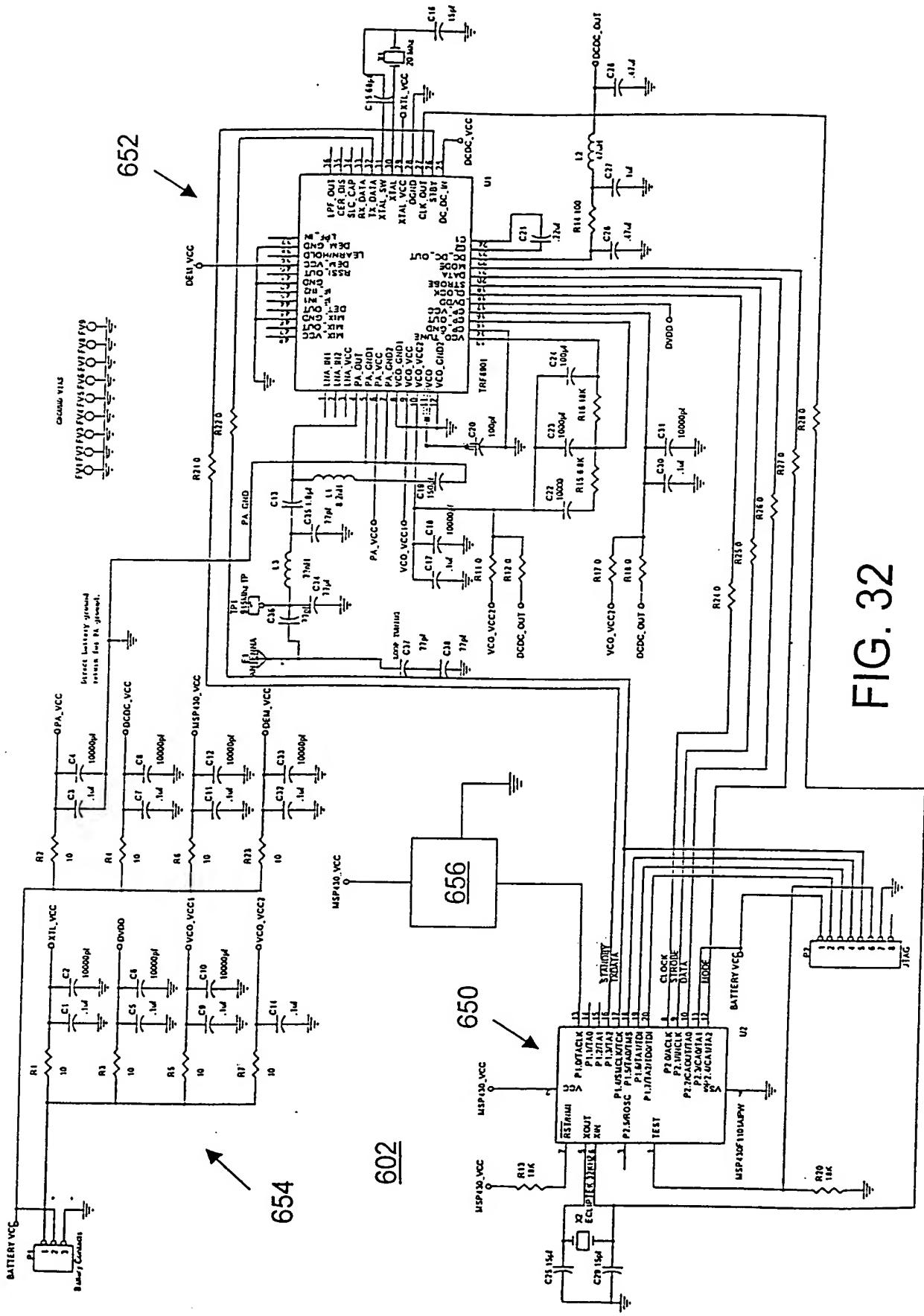


FIG. 32

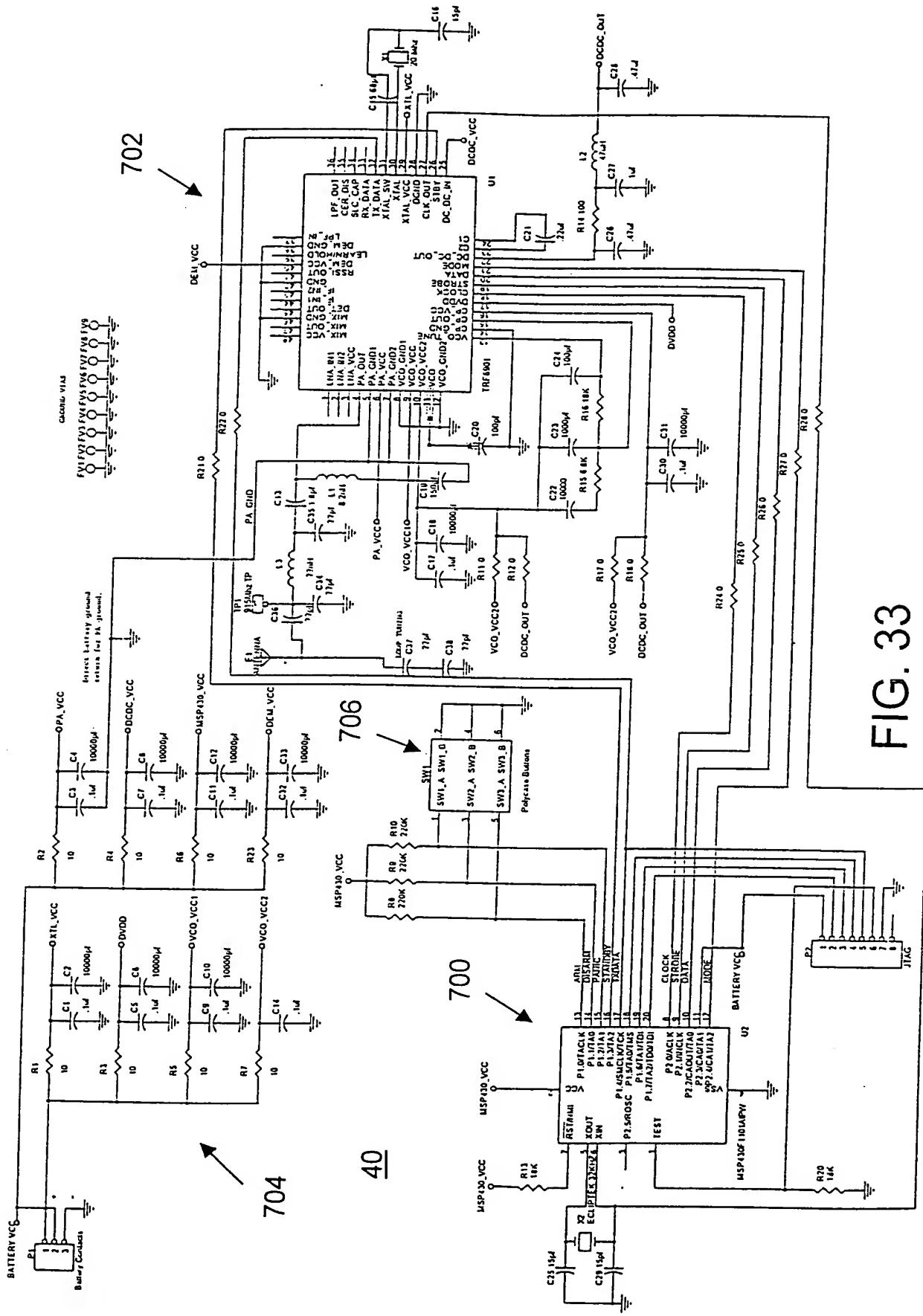


FIG. 33

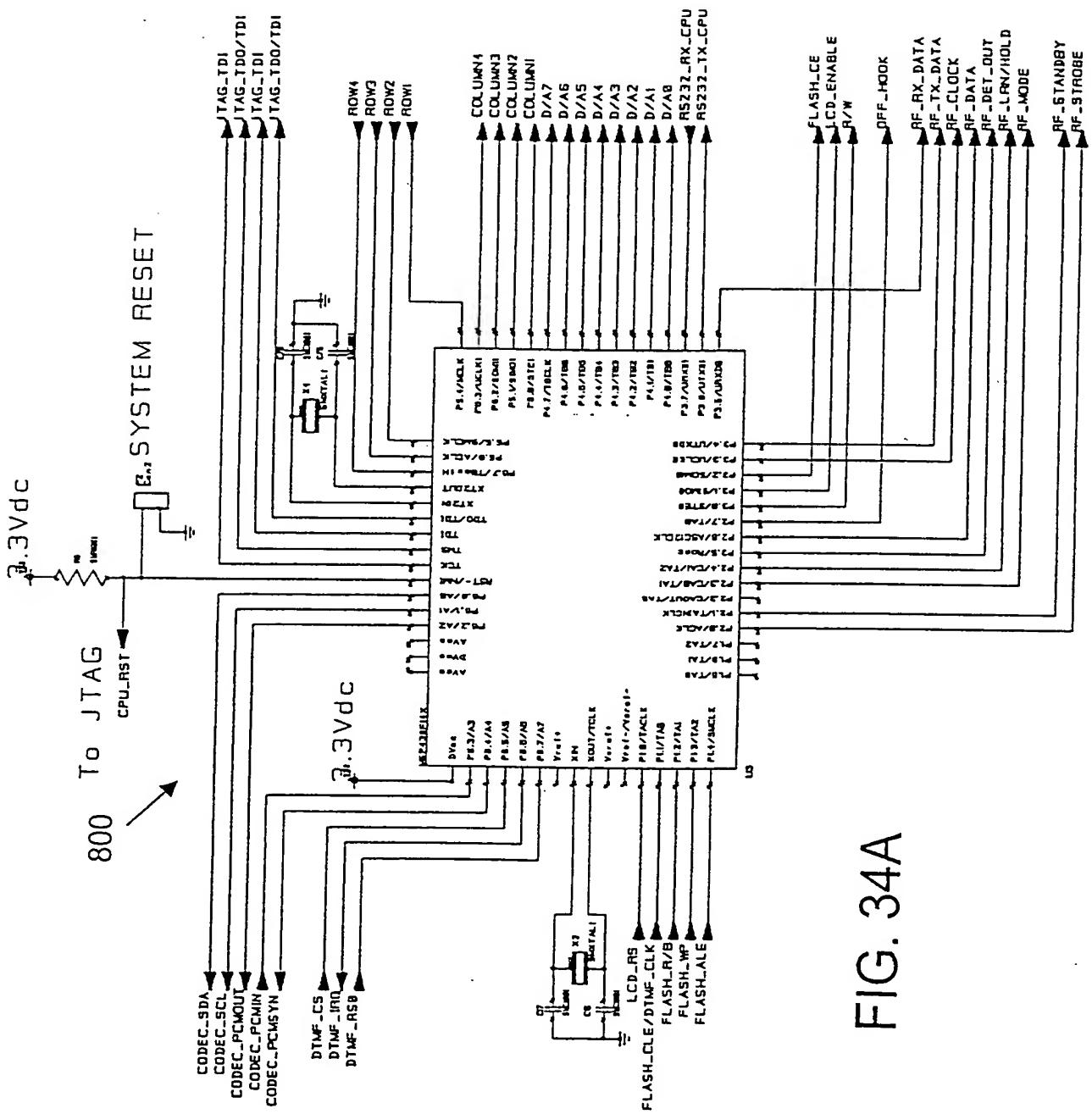


FIG. 34A

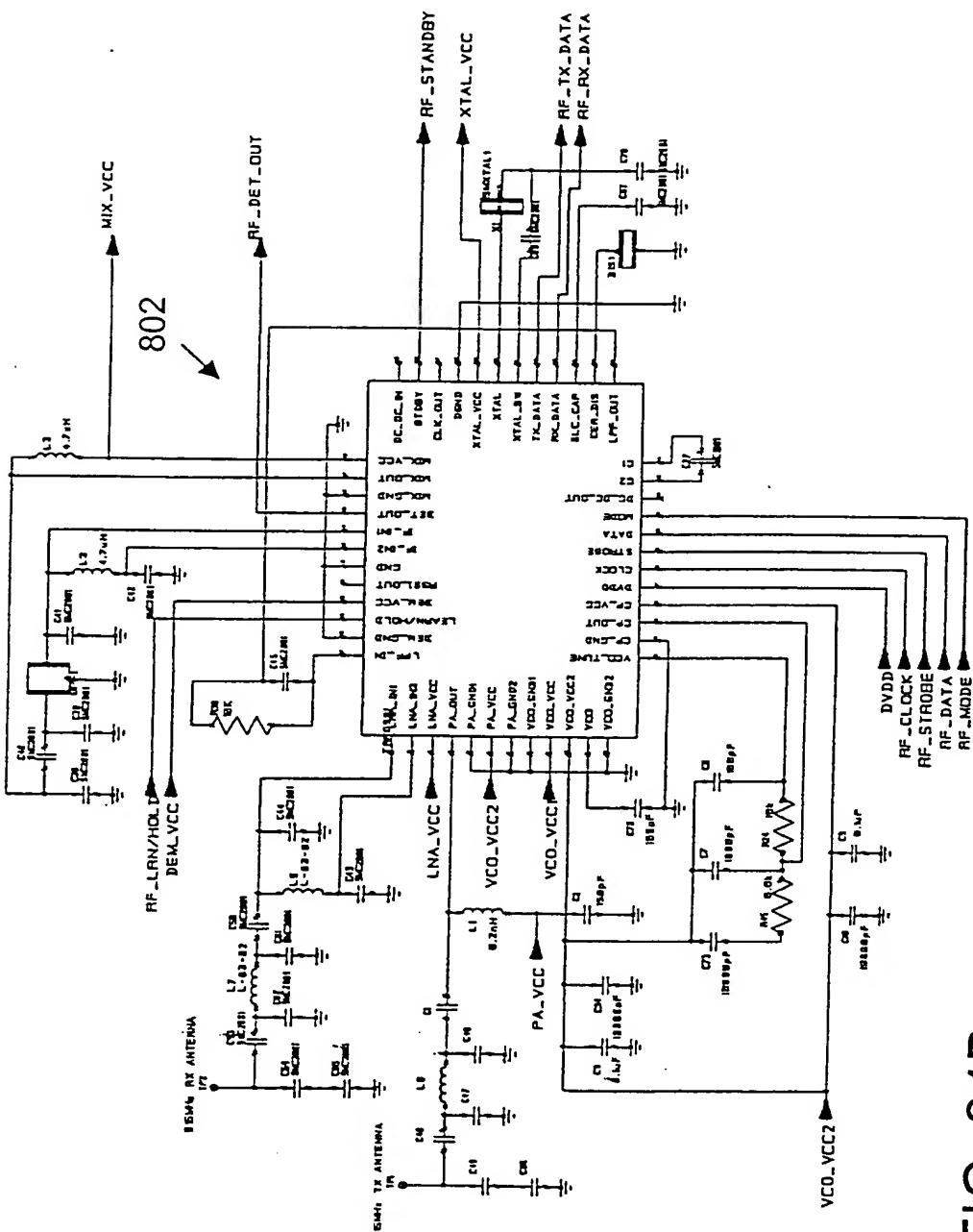


FIG. 34B

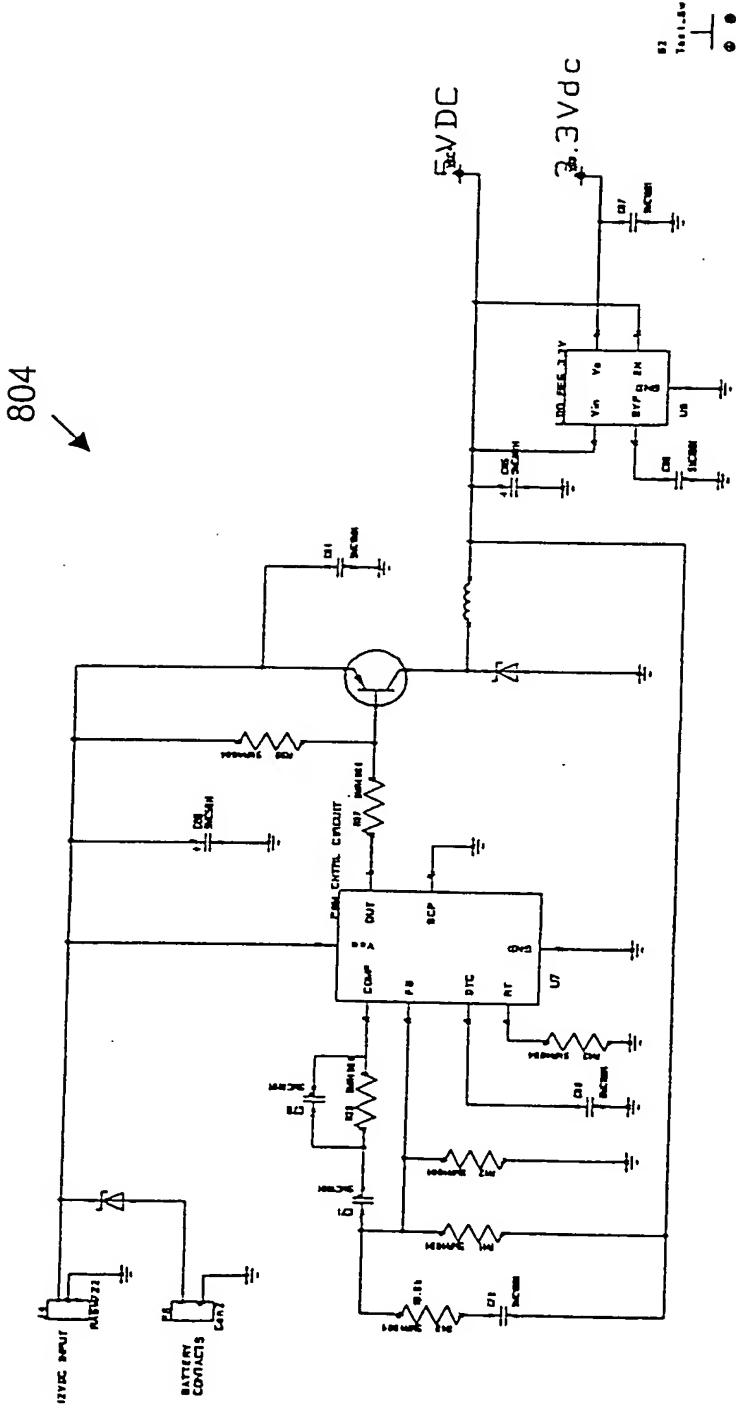
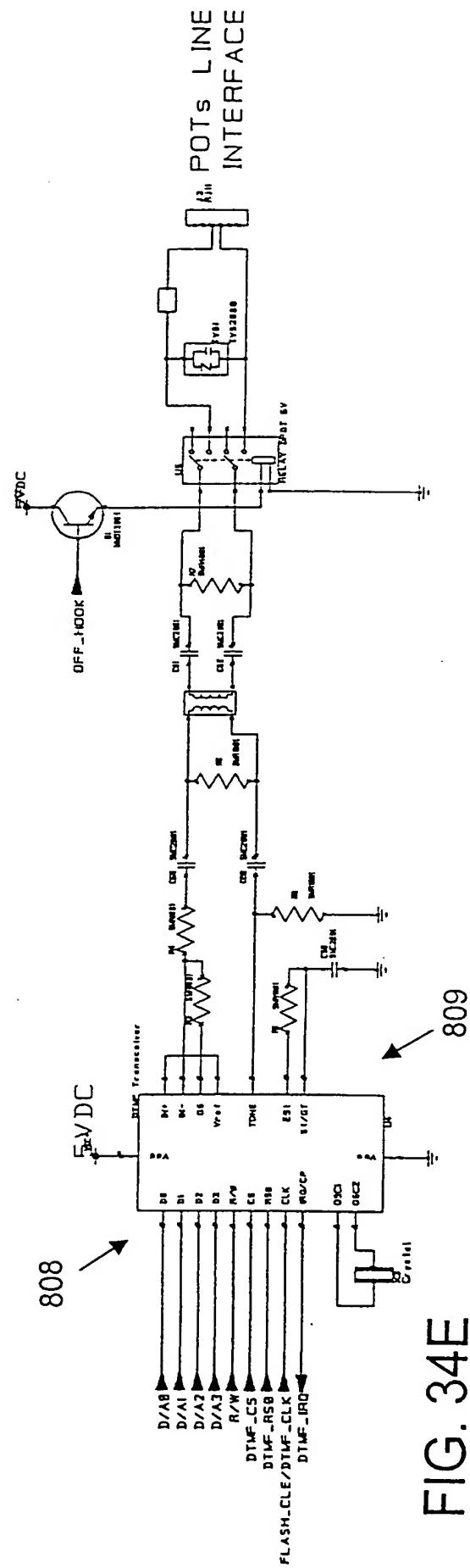
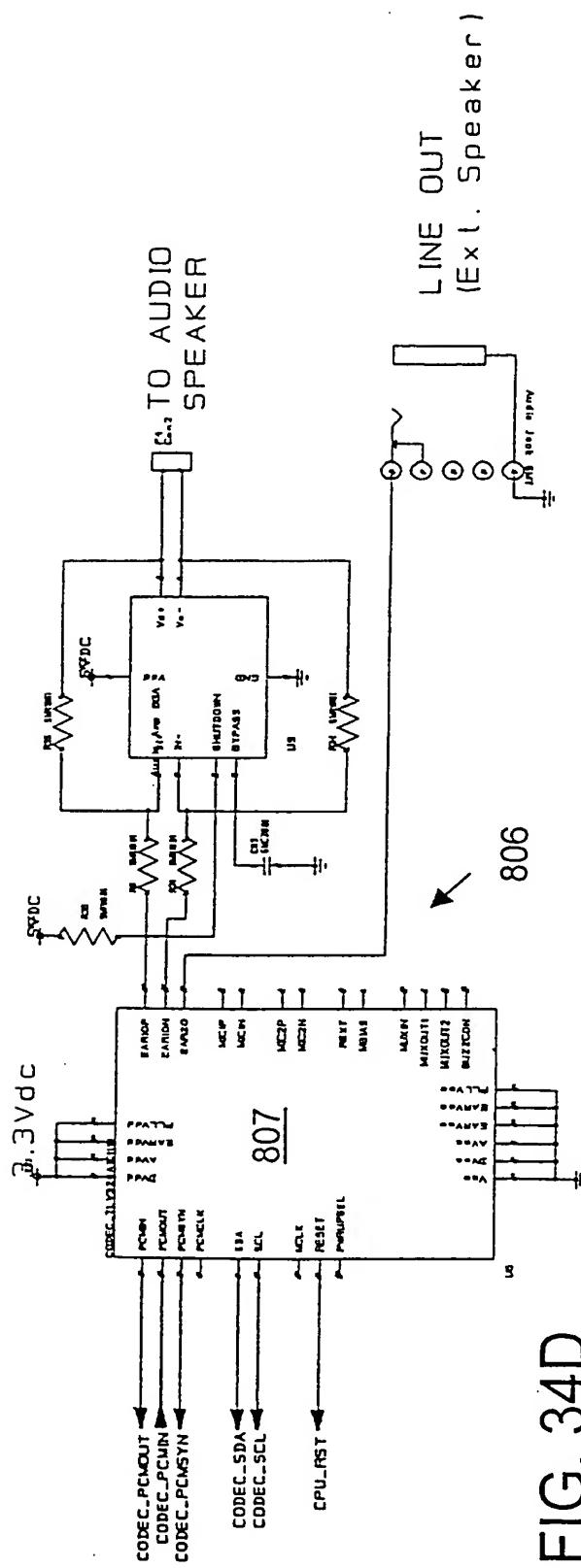


FIG. 34C



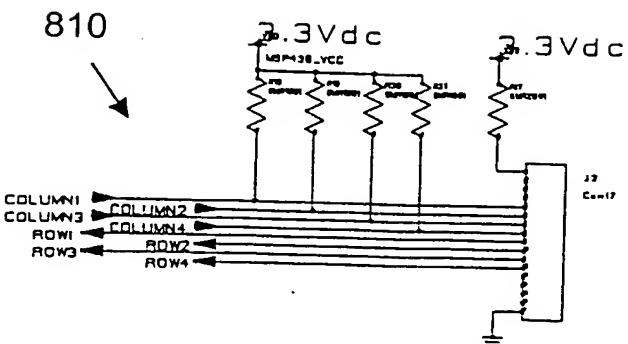


FIG. 34F

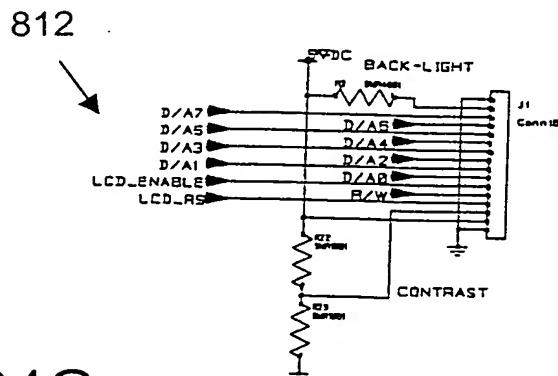


FIG. 34G

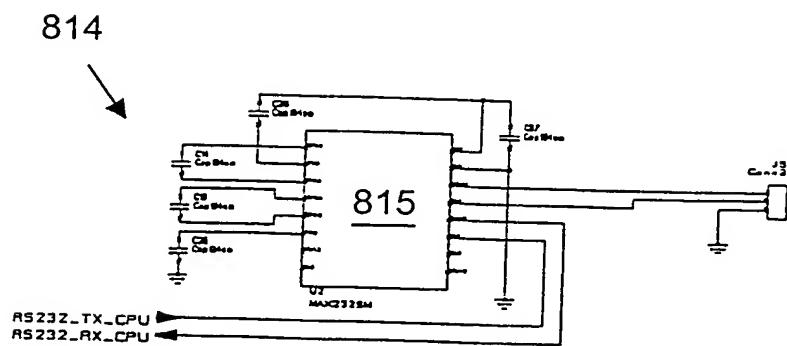


FIG. 34H

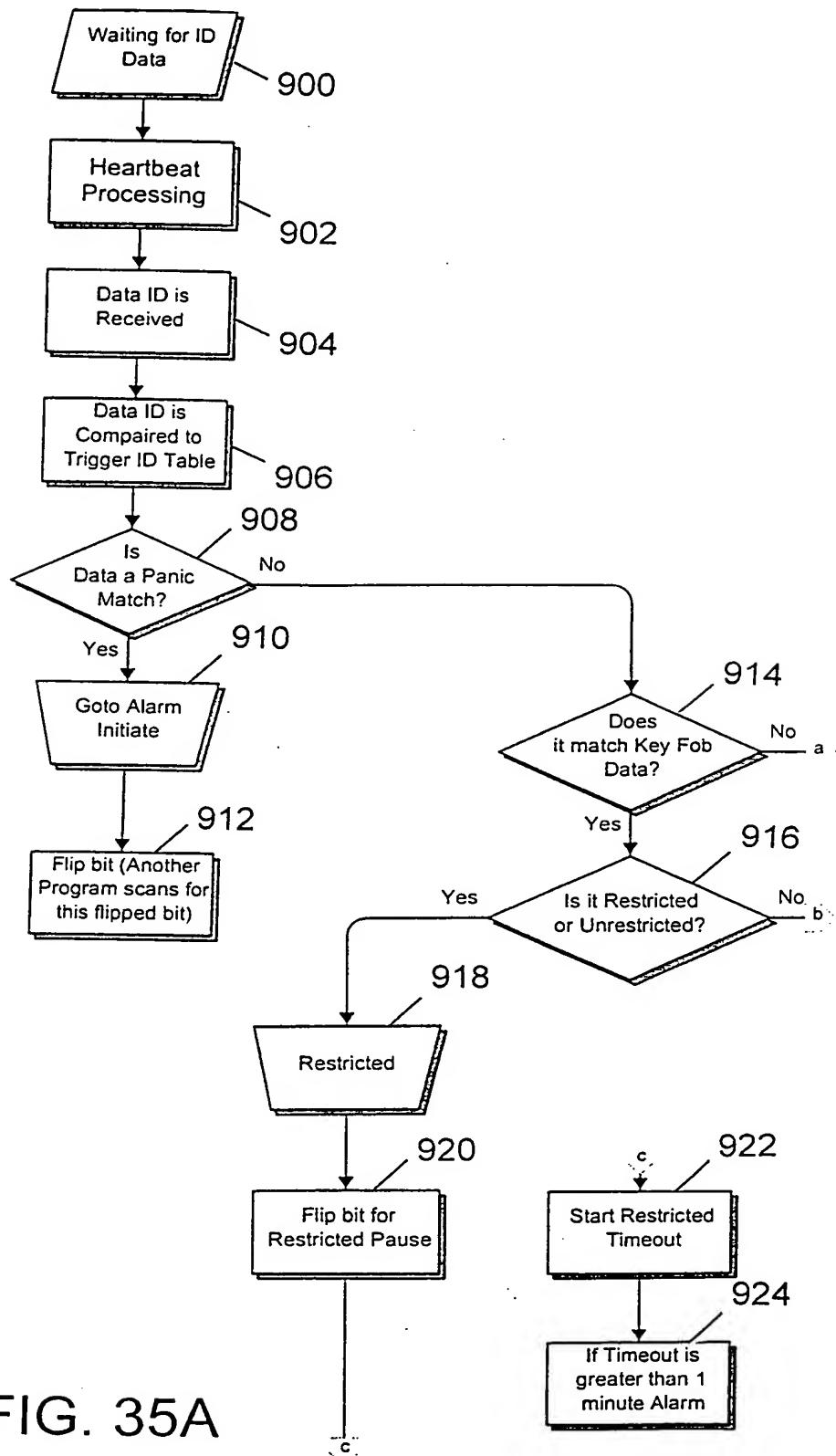


FIG. 35A

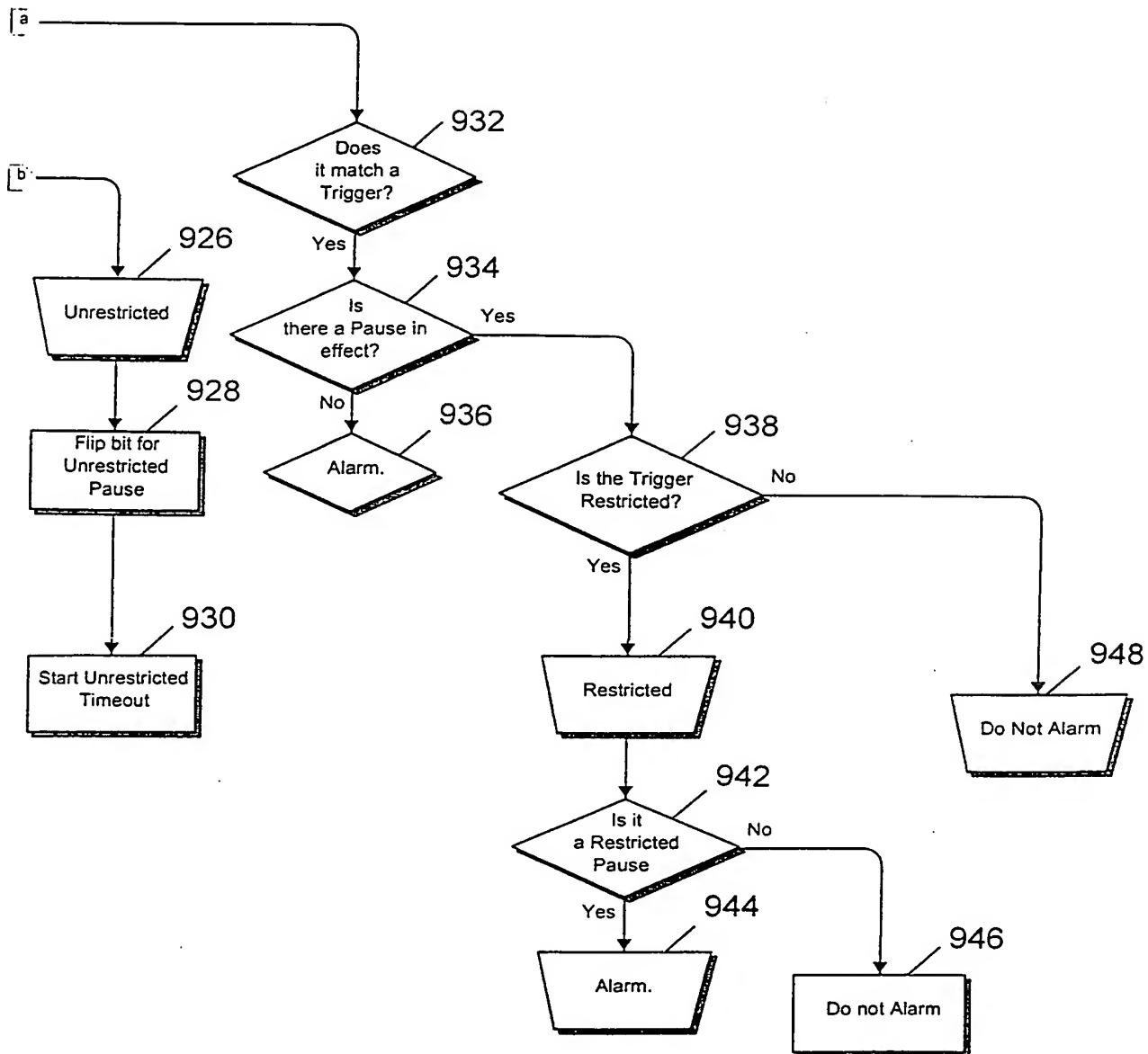


FIG. 35B

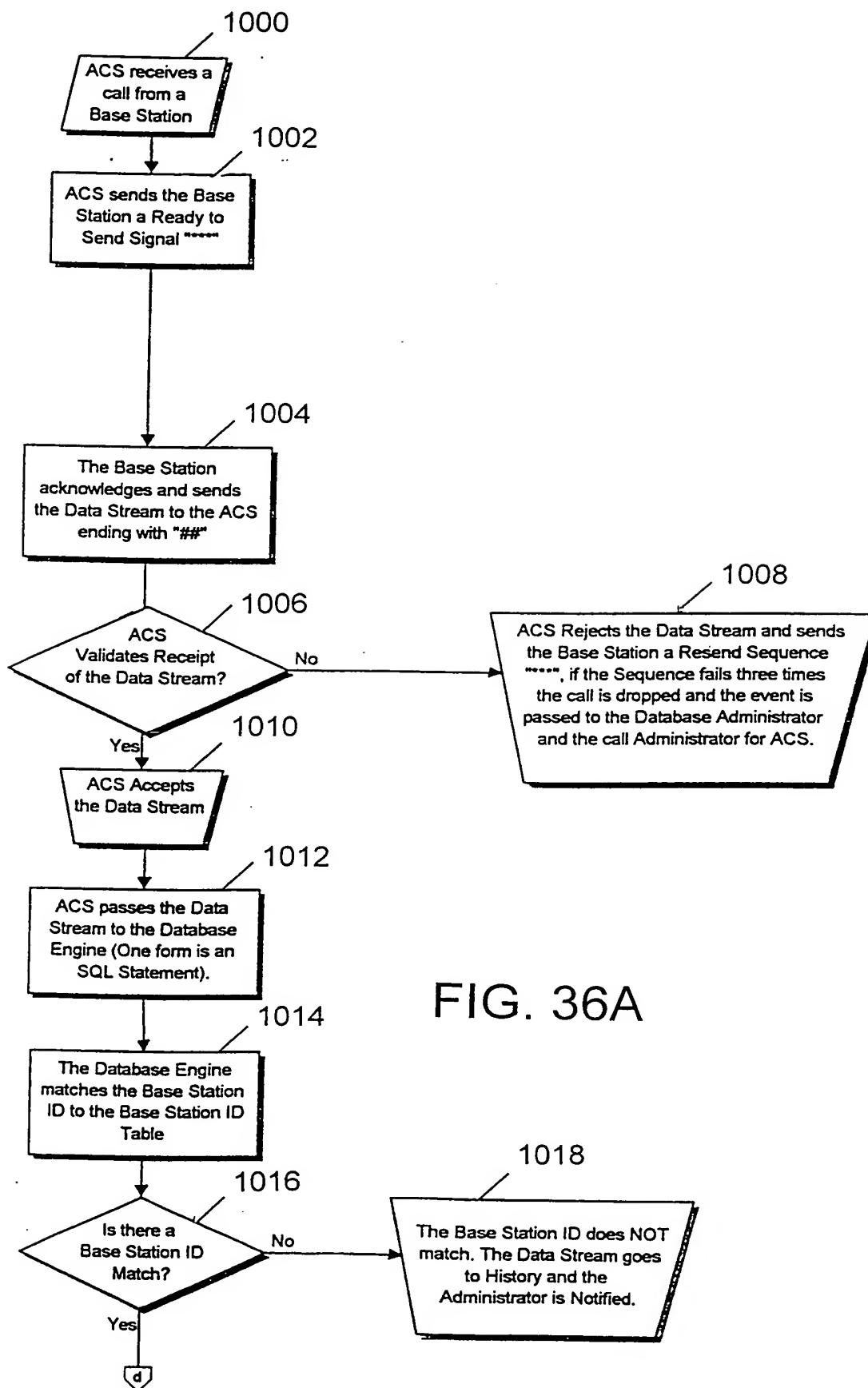


FIG. 36A

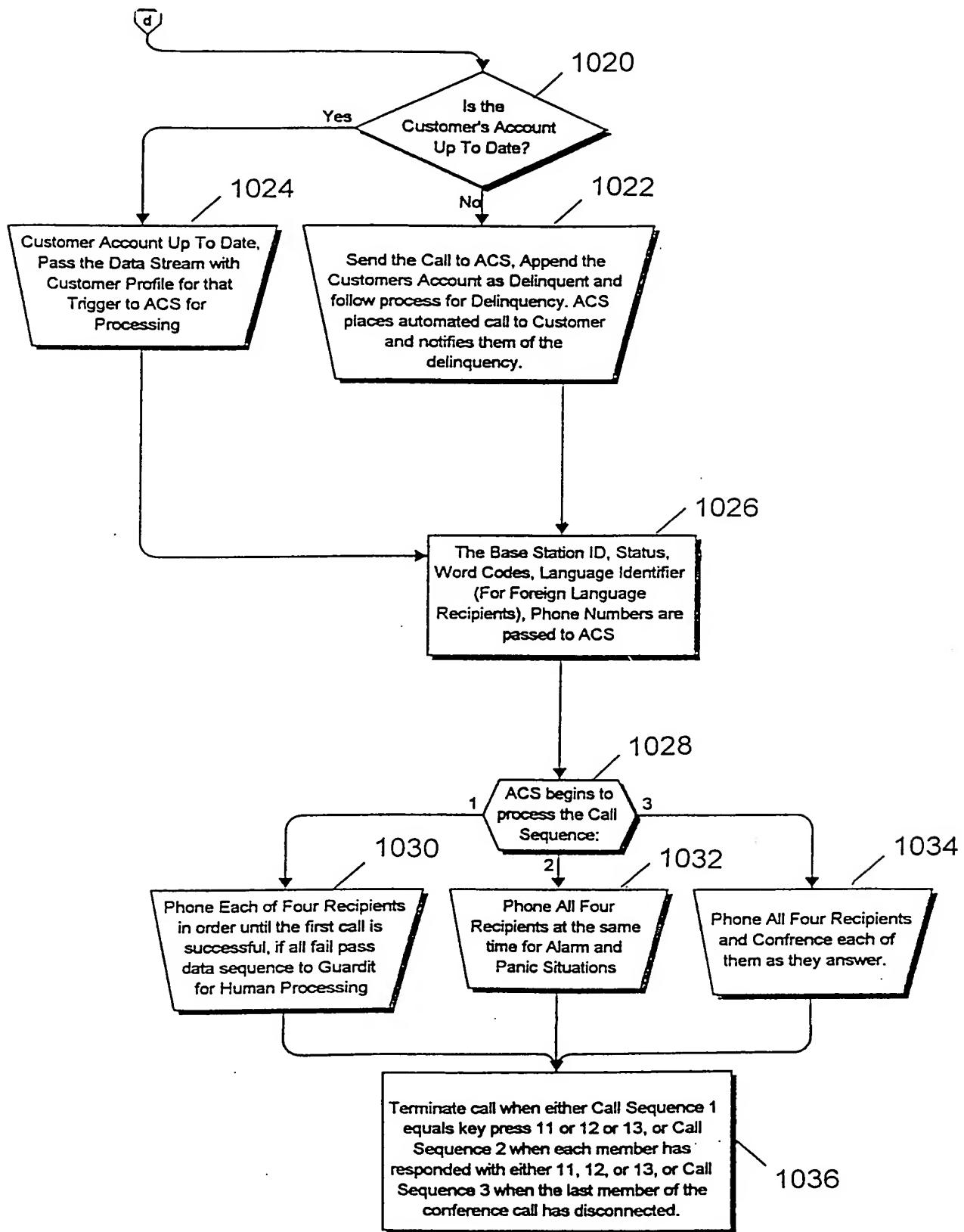
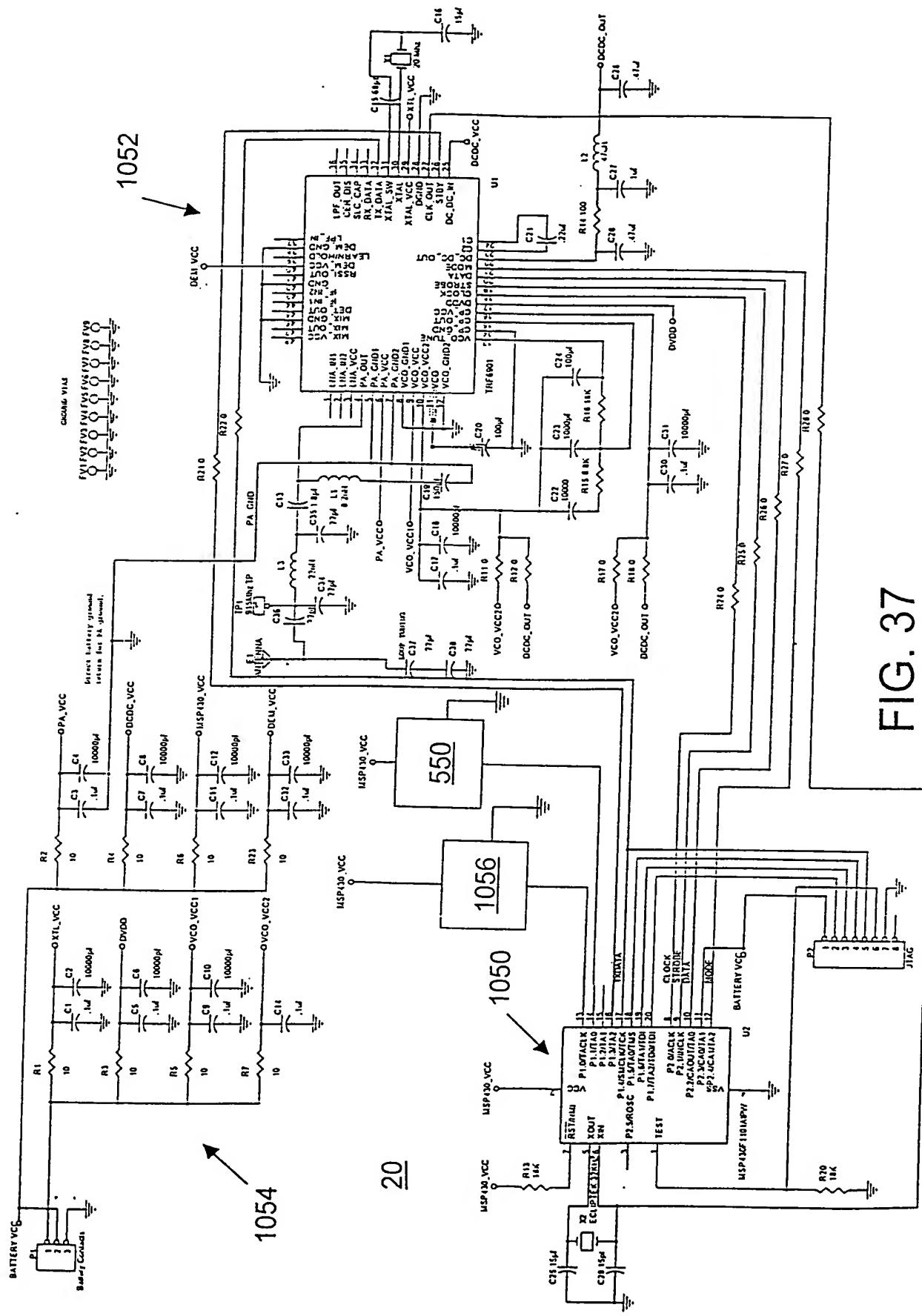


FIG. 36B



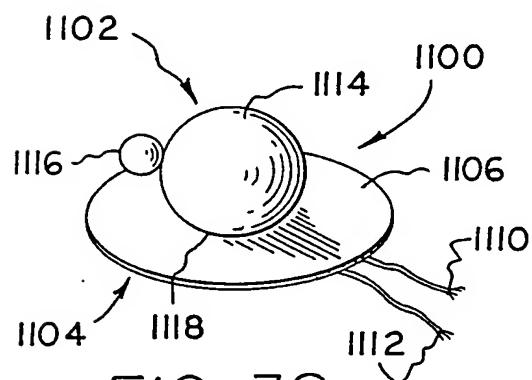
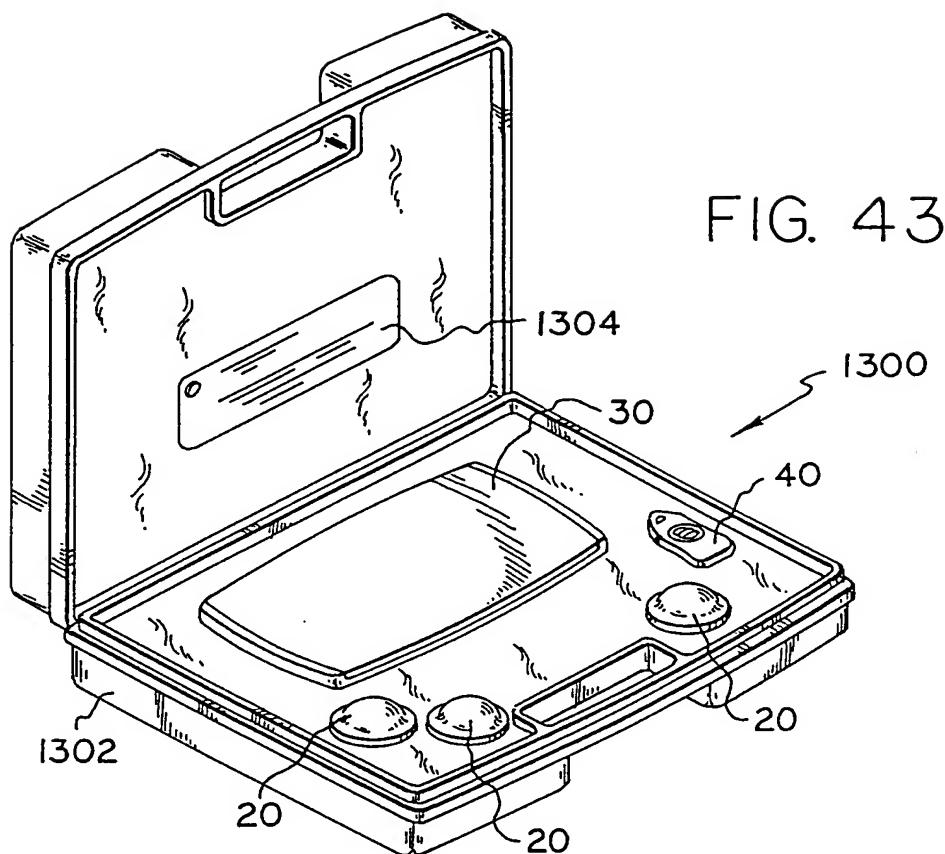


FIG. 38

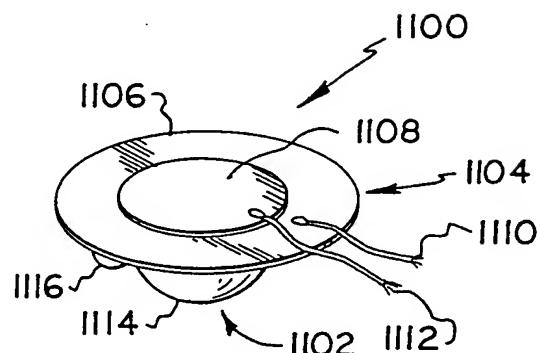


FIG. 39

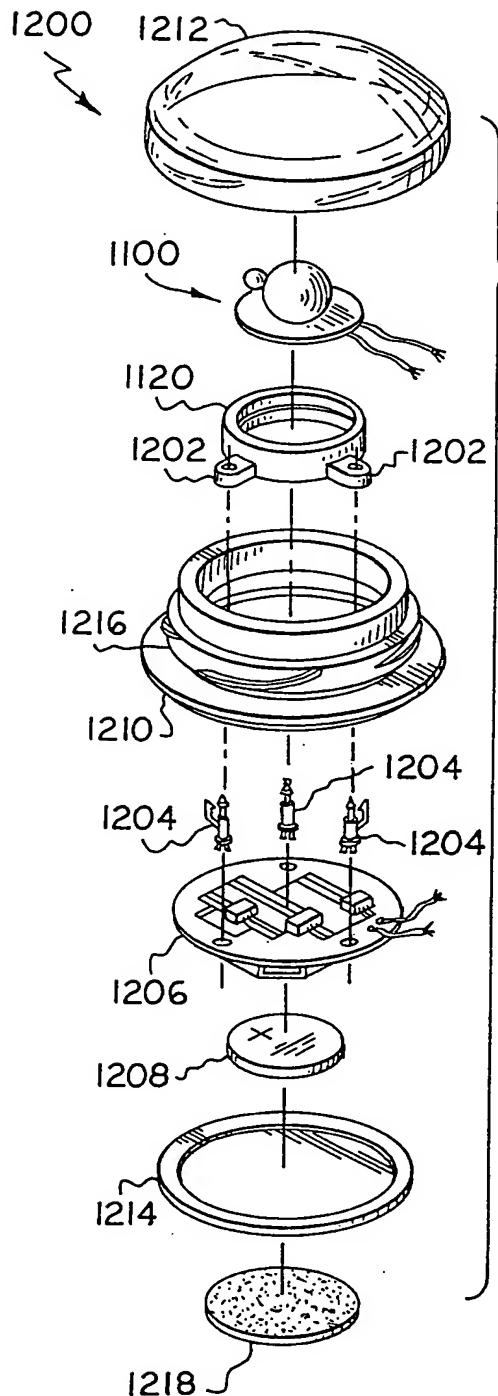


FIG. 42

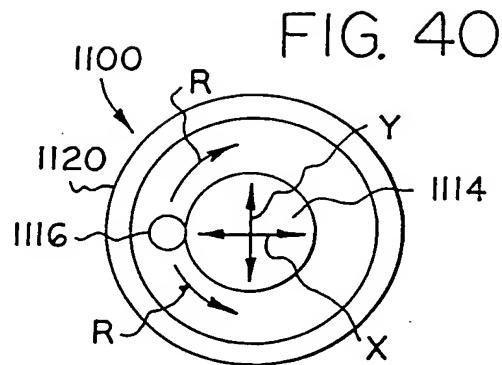


FIG. 40

